INSTRUCTION MANUAL

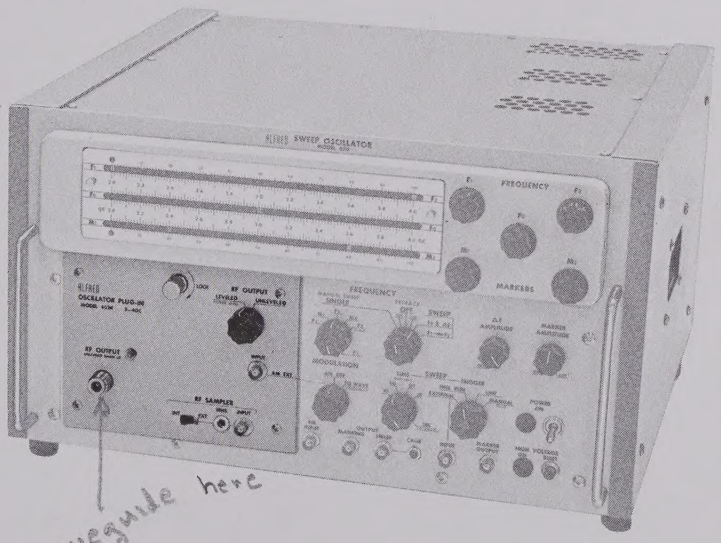
MODEL 650 8,0 - 12.4 GHz
SWEEP OSCILLATOR

655CKE MODEL plug-in units 1158 SERIAL

USING BWO TUBES WITH PIN DIODE LEVELING

Tube type: Stewart ☐ Varian ☒

Leveling capability: Int-Ext ☒ Ext only ☐



has X-Band waveguide here

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Manual 650-6

Date	ECO#	Effective Serials	Revision
9-19-68	13770	All	In Section 6, A12 Parts List, insert Alfred Part No. 81-20, Diode, IN4385, as alternate usage for A12CR1 and A12CR3 (IN459).
~~10-28-8~~	~~13788~~	~~All~~	In Sections 4,5,6, and 7 delete all reference to the anode protection circuit A19 (assy. ~~951619~~; schematic 907307) which was formerly used with Varian Tubes only. A simple 10 ma fuse is now used with Varian tubes, as shown on schematic 907053 ~~dated 10/68~~. Add to parts list for PIN Diode Plug-in chassis assy: F1 121-27 Fuse, 1/100 A, ~~type 8~~AG, 250 V (used with Varian tubes only) CR1 ~~81-135~~ Diode, ~~EH 400~~ (used with Varian tubes only)

TABLE OF CONTENTS

Paragraph		Page
	SECTION 1 - GENERAL INFORMATION	
1.1	Scope of Manual	1-1
1.2	Description of Plug-in Unit	1-1
1.3	Other Documentation Furnished with Manual	1-1
	SECTION 2 - INITIAL INSTRUCTIONS	
2.1	General	2-1
	SECTION 3 - OPERATING INSTRUCTIONS	
3.1	Removal, Installation, and Interchanging of Plug-in Units in the Model 650	3-0
3.2	Functions of Panel Controls, Indicator, and Connectors	3-0
3.3	Function of Time Meter	3-0
3.4	Operating Procedures	3-0
	SECTION 4 - CIRCUIT DESCRIPTION	
4.1	Block Diagram Description	4-1
4.2	The Backward Wave Oscillator Tube	4-3
4.3	Tube Performance Monitoring	4-8
4.4	Frequency Stabilizing Input	4-8
4.5	Diode Shaper Board	4-8
4.6	Leveler-Amplifier	4-10
4.7	PIN Diode Attenuator Power Supply	4-12
4.8	Elevated Supply	4-13
4.9	Anode Protection Circuit	4-13
4.10	Low-Pass Filter	4-13
	SECTION 5 - MAINTENANCE	
5.1	Periodic Maintenance	5-0
5.2	Voltage and Current Measurements at Chassis Test Points	5-0
5.3	Internal Adjustments	5-0
5.4	Trouble Shooting	5-7
5.5	Parts Replacement	5-8
	SECTION 6 - PARTS LIST	
	SECTION 7 - SCHEMATIC DIAGRAMS	

LIST OF ILLUSTRATIONS

Figure		Page
	SECTION 4 - CIRCUIT DESCRIPTION	
4-0	Simplified Diagram of RF Output and Level Control Circuits	4-2
4-1	Backward Wave Oscillator Tube	4-4, 4-5
4-2	Operating Characteristics of BWO Tube	4-6
4-3	Simplified Schematic and Operating Characteristics of Diode Shaper Board	4-9
	SECTION 5 - MAINTENANCE	
5-1	Top View of Plug-in Unit Chassis	5-1
5-2	Bottom View of Plug-in Unit Chassis	5-2
5-3	Basic RF Test Set-up	5-8
5-4	Power Calibration Test Set-up	5-11
	SECTION 7 - SCHEMATIC DIAGRAMS	
	Schematic Diagram Index	7-0

LIST OF TABLES

Table		Page
1-1	Specifications of Oscillator Plug-in Units	1-0

NOTE: This Manual Applies to 650 Models with C Suffix

Table 1-1. Specifications of Oscillator Plug-in Units.

Frequency GHz.		650 Model No.†	** Tube Manufacturer	Tube No.	Power Output (mw)yy	Power Variation Leveled dbtt	Residual FM (Max.Khz.)	Diode Shaper PC Board	Samplery	Sampler Attenuator	Low Pass Filter
1 to 2		651C* 651CK*	Stewart	SE 214C	60	±0.3	30	950636	951099	1151x	256-6y
			Varian	VA 183A	40			951600			xxxx
1.4 to 2.5		651C-S1 651CK-S1	Stewart	SE 219-3	50	±0.3	30	950637	950781	1151-1x	
			Varian	VA-186AA	50						xxxx
1.7 to 4.2		652C-S5 652CK-S5	Stewart	SE 215-6	15	±0.5	30	950858	950908		256-9y
			Varian	VA-184AA	15						xxxx
2 to 4		652C 652CK	Stewart	SE 215A-2	40	±0.3	30	950638	951100	1152x	256-2y
			Varian	VA 184A	30			951601			xxxx
3.5 to 6.75		653C-S1 653CK-S1	Stewart	SE 221-1	20	±0.4	50	950640	950783	1153-1x	256-4y
			Varian	VA 185AA	15			951602			xxxx
3.7 to 8.3		653C-S2 653CK-S2	Stewart	SE 211B-7	5	±0.5	50	950965	950896		256-8y
			Varian	VA 185AB	5			951610			xxxx
4 to 8		653C 653CK	Stewart	SE 211A-B	15	±0.5	50	950639	950784	1153x	256-1y
			Varian	VA 160A	15			950932			
7 to 11		654C 654CK	Varian	VA 173A	10	±0.5	75	950641	951101		256-5y
			Watkins-Johnson	WJ2039-3	10			951699			
7 to 12.4		654C-S1 654CK-S1	Varian	VA 173A	10	±0.75	75	950647	951101		256-5y
			Watkins-Johnson	WJ 2039-3	10			951603			
8 to 12.4		655C 655CK	Varian	VA 173E	20	xxx	75	950642	950786		256-5y
			Watkins-Johnson	WJ 2039-2	20			951604			

* C suffix indicates PIN DIODE Attenuator

**All Varian tubes use Anode protection circuits.

***Flap attenuator

x For External use with Model 650 only

xxx ±0.5 (8.2 to 12.4 Ghz.); ±0.7 (8.0 to 12.4 Ghz.)

xxxx Self-contained in BWO

† K suffix indicates rf leveling sampler mounted internally. Basic model requires an external sampler for leveling.

tt Level variation limits shown for units with internal leveler-sampler can be achieved with at-the-load leveling in all units, by use of a suitable external sampler.

y Used only on units (with K suffix) having internal sampling.

yy Upper figure indicates rated maximum unleveled output from units without internal sampling; lower figure indicates rated maximum leveled output from units with internal sampling.

PULSE MODULATION VOLTAGE REQUIRED AT AM EXT JACK: The pulse modulation voltage required for external on-off modulation varies with the response speed required. A signal of + 10V will turn the rf output on with a rise and fall time of about 1 μs in all units, in the unleveled mode. For high-speed, ac-coupled pulse modulation in the unleveled mode with rise and fall time of ≈ 0.1 μs, a pulse amplitude of typically +45 volts with 50 Ω source impedance is required in all units having Stewart (or Watkins-Johnson) BWO tubes; and a pulse amplitude of typically + 100V with 50 Ω source impedance is required in all units having Varian BWO tubes. During leveled operation, a signal of + 10V will turn the rf output on with a rise time of about 10 μs in units having PIN diode leveling, and about 20 μs in units having grid leveling.

Specifications subject to change without notice. Data not shown was unavailable at press time.

SECTION 1

GENERAL INFORMATION

1.1 SCOPE OF MANUAL.

This manual covers the group of Oscillator Plug-in units of the ALFRED Model 650 Series using BWO tubes with PIN diode leveling. All standard plug-in units covered by this manual are listed in Table 1-1.

Table 1-1 also includes performance information pertinent to each plug-in unit, and identifies the components which constitute the main differences between each plug-in unit of the group. These are the backward-wave oscillator (BWO) tube, the diode shaper board which matches the high voltage cathode supply output to the tube characteristic, the rf output low-pass filter and the internal sampler for K-suffix plug-in units. These component differences are required for operation in the different frequency bands shown in Table 1-1.

This manual should be used as a supplement to the basic Model 650 Instruction Manual. That manual includes all necessary information for operating the Model 650 with any plug-in unit installed. This plug-in unit manual gives supplemental descriptive, theory, and maintenance information pertinent to the plug-in units using BWO tubes with PIN diode leveling.

1.2 DESCRIPTION OF PLUG-IN UNIT.

Each plug-in unit is a modular rf head assembly designed to operate with the ALFRED Model 650 main frame to constitute a microwave sweep oscillator. All plug-in units can be operated interchangeably in any Model 650, and no adjustments are required, either on the Model 650 or the plug-in unit, when interchanging plug-in units. Thus, by using several plug-in units interchangeably, a versatile, wide-range oscillator capability is provided at minimum expense, and operation can be extended into additional bands when desired by purchasing additional plug-in units.

The plug-in units using BWO tubes with PIN diode leveling, covered in this manual, operate in frequency bands from 1 GHz to 12.4 GHz as listed on Table 1-1. Other plug-in units are available covering frequencies from 0.25 GHz to 40 GHz (see Table 1-2 in the Model 650 manual). All the plug-in units are equipped with an internal leveler-amplifier, and can provide leveled rf output when used with an external sampler. Those plug-in units with model numbers having a "K" suffix are also equipped with an internal sampler to permit self-contained leveling. These units have a front panel RF SAMPLER INT-EXT switch to select either internal or external sampling.

The plug-in unit chassis is fabricated from aluminum to avoid interference with the magnetic field of the BWO tube, and the top and bottom panels of the plug-in unit completely enclose the tube for shielding and mechanical protection. The tube and the plug-in leveler-amplifier PC board are mounted on a center plate in the forward portion of the chassis. The diode shaper plug-in PC board is located in the rear compartment of the chassis.

The frequency dial plate corresponding to the rf output band of each plug-in unit is stored in the top storage compartment of that plug-in unit, as shown in Figure 2-1 of the Model 650 Instruction Manual. Installation of this plate onto the dial plate area of the Model 650 calibrates the Model 650 for operation with the corresponding plug-in unit.

1.3 OTHER DOCUMENTATION FURNISHED WITH MANUAL.

An envelope at the rear of this manual contains factory performance-test data for the instrument and data for the BWO tube, as follows:

a. "ADDENDUM A" listing instrument model and serial number of the instrument and the particular values of factory-selected components used; and also listing the tube parameters pertinent to the tube originally shipped in the instrument.

b. A maximum power output performance plot of the plug-in unit made during final swept-frequency testing at ALFRED.

c. For units with internal leveling (K suffix models) a plot similar to b but showing performance in the leveled condition.

SECTION 2

INITIAL INSTRUCTIONS

2.1 GENERAL. Refer to Section 2 of the Model 650 Instruction Manual.

CAUTION

The BWO tube housing contains a strong permanent magnet. This magnet will attract magnetic materials brought near it, and the tube may be damaged (1) by being struck by such materials, and (2) by permanent alteration of its magnetic field by being exposed to such materials. Use only non-magnetic screwdrivers and other tools when working around the plug-in unit, and remove your wrist watch to avoid magnetic damage to it. The plug-in unit should be kept at least six inches from magnetic materials.

When a plug-in unit is removed from the Model 650, or if a unit is received separately, store it in a suitable shielding container, such as the ALFRED Model 9000 Storage Container, or on wood and at least six inches from magnetic sources.

SECTION 3

OPERATING INSTRUCTIONS

Refer to Section 3 of the Model 650 Instruction Manual.

3.1 REMOVAL, INSTALLATION, AND INTERCHANGING OF PLUG-IN UNITS IN THE MODEL 650.

CAUTION

When changing Plug-in units, turn the POWER switch off and wait 15 seconds before removing the installed Plug-in unit and inserting the other Plug-in unit, to permit decay of the power supply voltages. Then carry out the standard turn-on procedure.

3.1.1 REMOVAL OF A PLUG-IN UNIT FROM THE MODEL 650. To remove a plug-in unit from the Model 650, release its panel LOCK knob by turning fully CCW, and then pull the unit out of the Model 650 by pulling on the LOCK knob. Remove the frequency dial plate for that plug-in unit from the Model 650 panel and attach to the underside of the storage compartment door in the top of the plug-in unit, using the knurled screws removed with the plate. (See Figure 2-1 of the Model 650 Instruction Manual.)

3.1.2 INSTALLATION OF A PLUG-IN UNIT IN THE MODEL 650. To install a plug-in unit in the Model 650, first remove the frequency dial plate from the storage compartment door of that plug-in unit and install it into the calibrated position on the Model 650 panel. (See Paragraph 2.6 and Figures 2-1 and 3-1 of the Model 650 Instruction Manual.) Make sure the plug-in unit LOCK knob is turned fully CCW so the lock arm will clear the Model 650 panel opening and will enter far enough to actuate the safety interlock switch. Then insert the plug-in unit through the Model 650 panel and lock in position by pushing in on the LOCK knob and turning it fully CW.

3.1.3 INTERCHANGING PLUG-IN UNITS IN THE MODEL 650. To interchange plug-in units, follow the procedure of Paragraphs 3.1.1 and 3.1.2 above. The Model 650 is properly calibrated for operation with the new plug-in unit by installation of the frequency dial plate for that unit. No adjustments are required.

3.2 FUNCTIONS OF PANEL CONTROLS, INDICATORS, AND CONNECTORS.

See Tables 3-1 and 3-2 of the Model 650 Instruction Manual. Controls and connectors on the plug-in unit are covered at the end of each table.

3.3 FUNCTION OF TIME METER

The time meter, visible just inside the opening on the right-hand side of the plug-in unit at the rear, is provided as an aid in monitoring the rated number of heater hours of the BWO tube. A deposited metallic strip in the meter tube erodes from the "0" to the "1" position linearly during a preset number of hours of heater operation, in accordance with the value of factory-selected resistor R5. The rated number of heater hours for the BWO tubes used in plug-in units covered by this manual is indicated in the Tube Warranty and Replacement Procedure sheet included at the back of this manual. Do not replace the BWO tube just because the time meter has moved full scale. Tube performance rather than time meter indication should be the criteria for tube replacement. Refer to Paragraph 5.4.1 for analysis of BWO tube failure indications.

3.4 OPERATING PROCEDURES

Section 3 of the Model 650 Instruction Manual includes detailed instructions for operation of the plug-in unit and its controls. See paragraph 3.12 of that manual for a discussion of operation with leveled rf output and use of the POWER LEVEL controls.

NOTE: When external leveling is desired on units without internal sampling, it may be necessary to add a lowpass filter externally, between the RF OUTPUT jack and the external sampler, to avoid ripples in the leveled output near the upper end of the band. Units with an internal sampler contain a built-in low pass filter which eliminates the need for an external filter when external leveling is used. Some Varian tubes (See Table 1-1) also contain a built-in filter.

SECTION 4

CIRCUIT DESCRIPTION

4.1 BLOCK DIAGRAM DESCRIPTION (see Figure 4-0)

A basic block diagram of the plug-in unit is included in Figure 4-1 (Sheet 2) of the Model 650 Instruction Manual, and the basic functions of the plug-in unit and its three PC Boards (A11, A12, and A13) are described in that manual, in Paragraph 4-1 and Table 4-1. A simplified block diagram of the level-control circuits is shown in Figure 4-0.

The backward-wave oscillator (BWO) tube located in the plug-in unit supplies the sweep oscillator rf output. By applying proper drive signals to the BWO tube, the plug-in unit converts the dc power and control signals supplied by the Model 650 chassis into proper-frequency rf output signals with the required modulation and/or leveling. Power supply outputs from the Model 650 are standardized for all plug-in units where possible; but because of the different cathode and anode biasing requirements of different-frequency BWO tubes, the level-controlling feedback for these two supplies is developed in the plug-in units.

The cathode supply voltage applied to the BWO tube controls the rf output frequency. The cathode supply output line passes through the Elevated Supply PC Board A12, where it is connected to the 0-line. It then connects directly to the BWO tube cathode. The cathode supply output varies from -150 V to -2500 V, as required to develop sufficient feedback through the diode shaper board to exactly cancel the linear 0 to +15 V frequency-determining control signal developed by the dc control circuits in the Model 650. The Diode Shaper PC Board A13 is shown in Figure 4-1 (Sheet 1) of the Model 650 Instruction Manual.

The diode shaper board monitors a low-voltage replica of the cathode supply output, provided through a string of feedback dropping resistors, within a range of 0 to -15 V. From this, the board develops a non-linear feedback signal which compensates for the non-linear cathode voltage-vs.-output frequency performance curve of the BWO and permits closed-loop frequency control with a linear dial and linear control voltages.

The BWO tube helix operates at near-ground potential, being connected to ground through 10 ohms* (A12R2 in parallel with A7R14), as shown in Figure 4-10 of the Model 650 Instruction Manual. The BWO tube collector** is tied to the +100 V supply through 10-ohm resistor A12R13 in the elevated supply.

The anode supply output line is coupled to the BWO tube through the elevated supply, as shown in Figure 4-11 of the Model 650 Instruction Manual. The elevated supply develops feedback across A12R11, A12R14 to control the anode supply voltage to the level required by each tube. A12R14 is factory-selected, and A12R9 is factory-adjusted to set the proper voltage.

The rf output level is controlled by two different methods used in combination, as shown in Figure 4-0. A solid-state leveler (PIN diode attenuator), driven by the Leveler-Amplifier PC Board A11 is used to provide variable attenuation in the rf output signal path to compensate for power level variations in the unleveled power output characteristic of the BWO tube. The PIN diode attenuator is also used to adjust over-all output power level in both the leveled and unleveled modes. Grid modulation of the BWO tube is used to provide full on-off rf output control for square-wave or pulse modulation. Since the PIN diode attenuator is used for all variable-level power control while the BWO tube continues to operate at full power, maximum frequency stability is achieved.

The grid voltage applied to the BWO tube controls the on-off rf output modulation. The grid voltage is developed by the circuit of A12Q1, as shown in Figure 4-16 of the Model 650 Instruction Manual. This circuit operates within the range of $V_{cathode}$ and -100 V below $V_{cathode}$. The transfer oscillator supplies zero current drive when no modulation signal is present. This cuts off A12Q1, bringing the grid close to cathode potential for full rf output power. In this condition, the only drop across A12L1 and A12R5 is that due to grid current.

To turn off the rf output during the "off" half-cycle of square wave modulation or during the "rf off" mode, the transfer oscillator generates a current of about 35 ma which develops a drop of 27 V across A12R5 to provide a -27 V grid-cathode bias for Stewart/WJ tubes, and a drop of 61 V across A12R4/R5 to provide a -61 V grid-cathode bias for Varian tubes. This cuts off the BWO electron beam completely, providing an ∞ on/off ratio.

For control of output level in the leveled mode, the output is attenuated during the sweep by a variable amount as required to produce constant power equal to that available at the point of minimum unleveled output. The leveler-amplifier receives a fixed-level reference signal from the modulator, which is adjusted by the POWER LEVEL control, and a feedback signal proportional to the instantaneous output level from the crystal detector of the internal or external sampler. The leveler-amplifier generates an analog (-) voltage signal for the PIN diode attenuator, related to the amount of level reduction required. This signal is applied to the PIN diode driver circuit which is an integral part of the PIN diode attenuator module. The driver supplies control currents to the leveling and matching diodes contained in the PIN attenuator structure. This rf structure then operates as an electrically-controlled microwave resistor, providing attenuation as necessary to reduce power to the required steady level.

The reference signal level from the modulator is altered only when modulation is simultaneously applied to the BWO tube, to prevent the leveler from attempting to correct for such desired modulation. The POWER LEVEL control adjusts this reference signal to achieve balance with the output from the crystal detector at the desired rf power level. This level may be set anywhere between the maximum output level (above full-band leveling) and 10 db below the maximum level available with full-band leveling, an over-all range of about 15 db.

Whenever the reference and crystal detector signals do not cancel, the differential input stage of the main leveling loop in the leveler-amplifier generates a correction signal, ϵ_1, which is amplified by A_1 in the main leveling loop and applied to the A_2 stage. The gain-equalizing loop modifies the error signal, ϵ_2, applied to the second half of the main leveling loop (A_2) to effectively adjust the main leveling loop gain for optimum response at any operating power level. The output from A_2, applied through the PIN diode attenuator circuits, controls the PIN attenuator to supply the necessary level-correcting output attenuation, and also controls the matching diodes to balance the instantaneous resistance of

* 100Ω on units having Stewart/WJ BWO tubes.

** Stewart/WJ BWO tubes only.

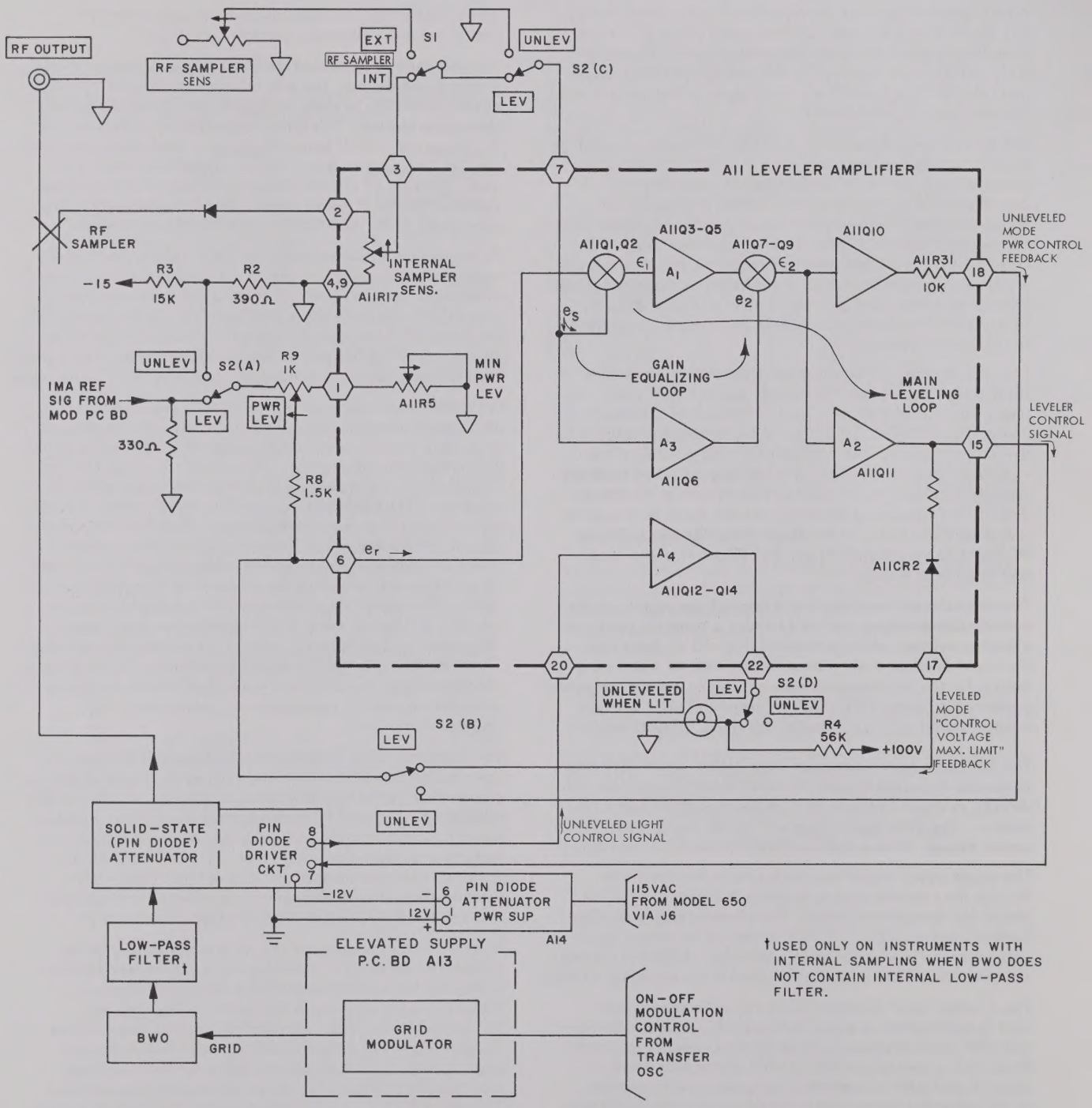


FIGURE 4-0. SIMPLIFIED DIAGRAM OF RF OUTPUT AND LEVEL CONTROL CIRCUITS

the leveling diodes so that the PIN attenuator rf network yields a fixed 50-ohm input and output impedance.

The sensitivity of the feedback-stabilized leveler-amplifier is high enough that it levels the rf output to within a fraction of a db regardless of the unleveled characteristic of the tube. As the output sweeps through frequencies where the unleveled characteristic is several db higher than the minimum-power point in the band, the leveler increases the correction signal in response to the increased crystal output, and prevents the level from increasing more than about 0.3 db.

Whenever power is leveled over the full range being swept, a negative signal is continually supplied from the PIN diode attenuator driver to a two-stage amplifier to cut off power from the UNLEVELED WHEN LIT light. The absence of a suitable driver output signal during any portion of the swept band, indicating that the rf output signal is totally or partially unleveled, is sufficient to allow this amplifier to energize the UNLEVELED light.

The signal level applied to the leveler-amplifier from the internal or external detectors is adjustable with individual SENSITIVITY controls to provide optimum response in the leveled loop.

For control of output level in the unleveled mode, the leveler-amplifier drives the PIN attenuator to provide a fixed output attenuation with a magnitude controlled by the setting of the POWER LEVEL control. The crystal detector signal to the differential input for A_1 is grounded, and the main leveling loop consisting of A_1 and A_2 acts as a single-ended amplifier for the level-control signal supplied by the POWER LEVEL control. The reference signal supplied to the POWER LEVEL control in this mode is developed by a voltage divider from the -15 V supply. The feedback signal for the unleveled mode is developed by the A11Q10 amplifier stage which samples the voltage supplied to A_2. This feedback permits closed-loop operation of the leveler-amplifier to provide the proper leveling control voltage (and therefore output attenuation) in response to any setting of the POWER LEVEL control.

4.2 THE BACKWARD WAVE OSCILLATOR TUBE (See Figure 4-1.)

The backward-wave oscillator (BWO) tube is a voltage-tunable microwave oscillator vacuum tube. It consists of an electron gun, a slow wave structure (helix), and a collector at the far end of the slow wave structure. The rf output of the tube is produced by the interaction between the electron beam passing from the gun to the collector end and the electromagnetic field of a microwave signal developed on the slow wave structure. In Stewart tubes the collector collects all electron beam current not intercepted by the helix. In Varian BWO tubes, an ion collector is located at the target end of the slow wave structure. The helix collects all electron current in the tube since there is no electron collecting electrode at the target end of the tube. The ion collector electrode, internally connected to the cathode, is provided to collect positive ions and establish a field gradient that drives the electron current outward for interception by the helix.

Surrounding the evacuated envelope of the tube is a cylindrical permanent magnet which produces a strong axial field. This field focuses the electrons in the beam and allows movement only in the direction of the axis of the tube.

An exterior housing completely covers the magnet and tube, and no magnet adjustments are possible. The tube electrodes are connected to wire leads which are brought out through the housing for connection to the Elevated Supply PC Board A12 of the plug-in unit.

The Stewart tube electron gun contains a heated cathode and two or more aperture disc type control electrodes (grid and anode) which help form the beam and control beam density. The Varian tube electron gun contains a cathode surrounded by an aperture disc control electrode (the grid), and two screen-type electrodes (the anodes) which accelerate, control, and form the beam. $Anode_1$ is brought out to an external connection. $Anode_2$ is internally connected to the helix. The heater is a helical coil contained inside the cathode support cylinder.

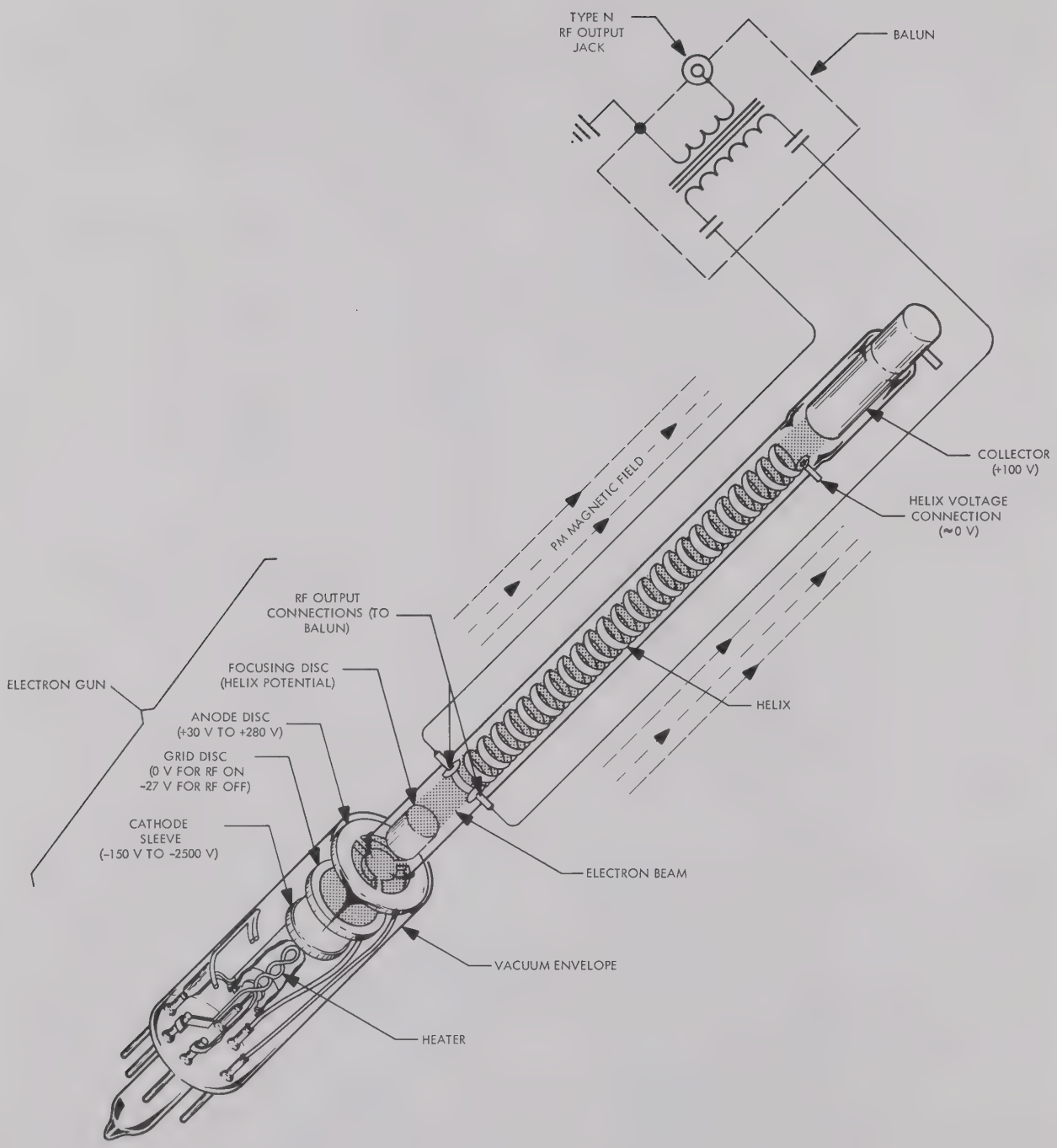
In a helix backward wave oscillator, it is desirable to have a hollow cylindrical electron beam since only the electrons that pass adjacent to the helix are fully effective in delivering energy to the wave. Electrons near the middle of the beam consume anode power but contribute little or nothing to the energy interchange that produces the backward wave oscillations. The electron gun elements are arranged to generate a hollow, circular electron beam. The magnetic field, which restricts the electrons to axial movement, then maintains the same geometry to achieve a hollow cylindrical beam throughout the length of the slow-wave structure.

The Stewart tube uses a sleeve-type cathode. In the Varian tube, the emitting portion of the cathode is a hollow cylinder having an L-shaped cross section to absorb the full radiant output of the heater and emit a thin, hollow, cylindrical electron beam having a diameter slightly less than that of the helix. The cold cathode, with a T-shaped cross-section, is fabricated from a non-emitting material and fills the remaining end surface of the cathode cylinder. The grid control field is developed between the cathode and grid disc, just outside the hot cathode ring. This provides effective control of the electron beam right at the emitting surface. The $anode_1$ voltage is maintained at a fixed positive level with respect to the cathode supply, so that the beam emitted from the electron gun does not change appreciably as the cathode voltage is changed to shift the operating frequency. (Since the anodes are of an intercepting type, care must be exercised not to apply too high a voltage or to permit too high a current.)

The helix is a slow-wave structure equal in length to several wavelengths of the lowest output frequency. Thus, the operating frequency range of a particular backward wave oscillator tube is primarily determined by the dimensions of the helix.

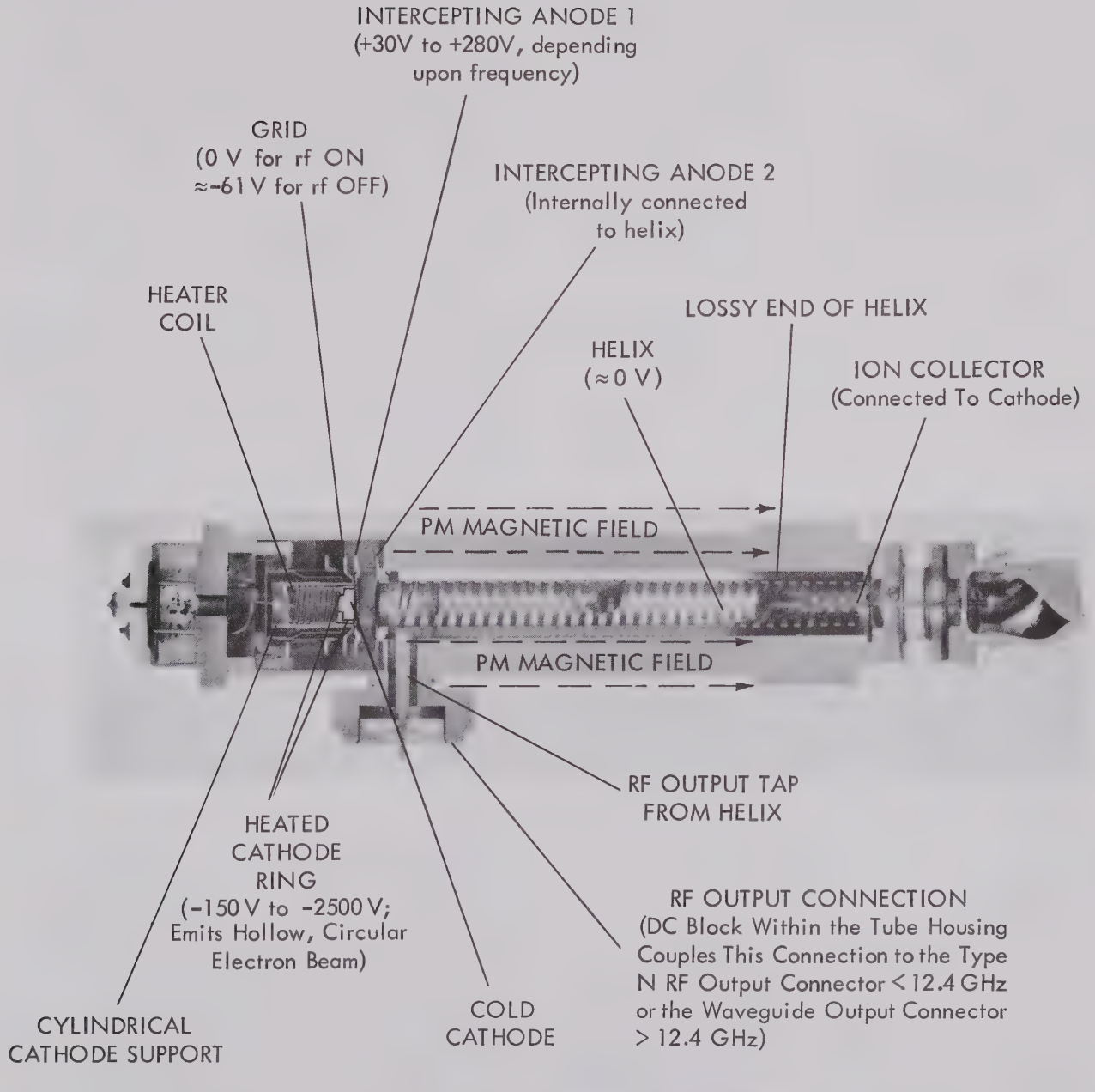
It is well known that a slow wave structure will propagate a particular range of frequencies at constant axial velocity (the non-dispersive region) and above that range, the axial velocity of propagation increases with frequency (the dispersive region). The backward wave oscillator operates in the dispersive region and it will oscillate at the frequency for which the electron beam velocity is very nearly equal to the axial velocity of propagation of the microwave signal on the helix. Figure 4-2A shows the variation of propagation velocity with frequency for a particular helix while Figure 4-2B shows how the beam velocity varies with the helix-to-cathode voltage.

From these figures it can be seen why the backward wave oscillator can be voltage tuned. By changing the helix-to-cathode voltage, the electron beam velocity changes, thereby changing the frequency for which the velocity of propagation on the helix and the velocity of the beam are in synchronism. The helix dimensions, primarily diameter and pitch,



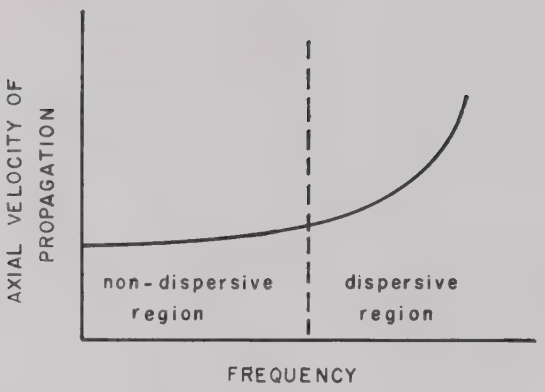
A. STEWART BWO TUBE

FIGURE 4-1. BACKWARD WAVE OSCILLATOR TUBE


INTERCEPTING ANODE 1
(+30V to +280V, depending upon frequency)
GRID
(0 V for rf ON
≈-61 V for rf OFF)
INTERCEPTING ANODE 2
(Internally connected to helix)
HEATER COIL
LOSSY END OF HELIX
HELIX
(≈0 V)
ION COLLECTOR
(Connected To Cathode)
PM MAGNETIC FIELD
PM MAGNETIC FIELD
RF OUTPUT TAP FROM HELIX
HEATED CATHODE RING
(-150 V to -2500 V; Emits Hollow, Circular Electron Beam)
RF OUTPUT CONNECTION
(DC Block Within the Tube Housing Couples This Connection to the Type N RF Output Connector < 12.4 GHz or the Waveguide Output Connector > 12.4 GHz)
CYLINDRICAL CATHODE SUPPORT
COLD CATHODE

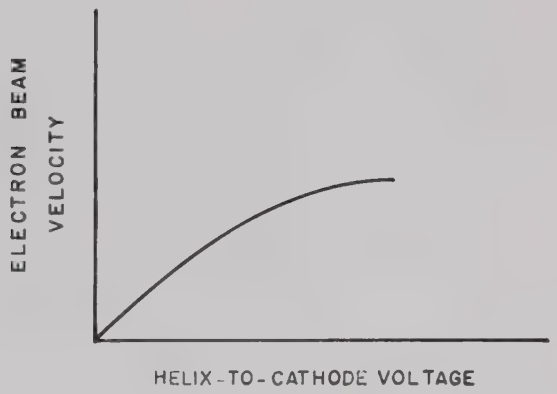
B. VARIAN BWO TUBE

FIGURE 4-1. BACKWARD WAVE OSCILLATOR TUBE



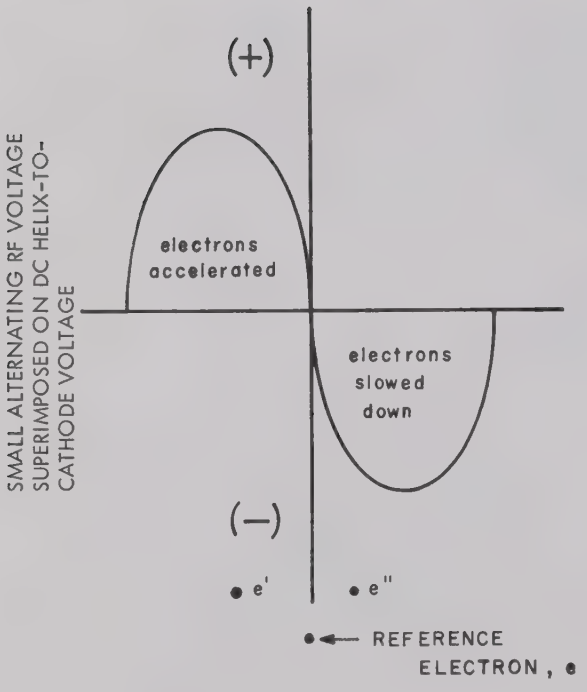
AXIAL VELOCITY OF PROPAGATION VS. FREQUENCY

(A)



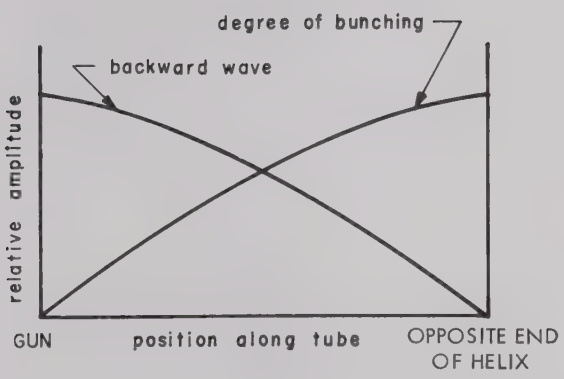
ELECTRON BEAM VELOCITY VS. HELIX-TO-CATHODE VOLTAGE

(B)



ELECTRON DISTRIBUTION

(C)



RELATIVE AMPLITUDE VS. POSITION ALONG TUBE

(D)

FIGURE 4-2. OPERATING CHARACTERISTICS OF BWO TUBE

are chosen so that the tuning voltages required for a particular frequency range are reasonable (usually about 200 to 2000 volts).

The oscillations, and hence the rf wave, begin in the backward wave oscillator in much the same manner as they begin in other oscillators. Noise waves are established on the helix from the shot noise coupled from the electron beam and from thermal energy developed at the input end of the tube. Rf waves are produced which travel in both directions in the helix i.e., from gun to collector and from collector to gun. Since the desired wave in a backward wave oscillator must travel from the collector to the gun end, attenuators are placed in the tube which eliminate the forward-moving waves. Amplification of the rf wave is produced by the interaction of the electrons in the beam and the electromagnetic field of the wave.

To obtain this interaction, the electron beam must be at some critical velocity which is achieved by applying a dc voltage to the helix. Because the rf electromagnetic field is alternating, electrons in the beam are subjected to either an accelerating or decelerating field. Thus, the beam electrons passing axially near the helix experience velocity modulation, with a tendency for bunches of electrons to be formed as the beam moves through the helix. The critical areas of interaction are adjacent to the spaces between the helix turns because the axial electric field is strong between helix turns and weak under the turns.

To explain the bunching process let us examine a group of electrons as they travel from the gun to the opposite end of the tube. Within this group there will be one electron, e, which corresponds to the zero point of the alternating axial field and is therefore unaffected by the signal on the helix. See Figure 4-2C. This electron experiences no acceleration from the electromagnetic field. However, electron e', just to the left of e, encounters a positive axial field and is accelerated, thus tending to catch up with e. Similarly, electron e", which is just to the right of e, encounters a negative or decelerating field and so slows down and tends to be overtaken by electron e.

The electrons centered about e are thus velocity modulated. This situation is repeated over and over again as the electrons centered about the reference electron pass one space after another enroute from the gun to the lossy end of the helix.

If the helix-to-cathode voltage is set to achieve a beam velocity such that at the collector end of the tube the phase difference between the wave and bunches in the beam is approximately one half cycle, the electron bunches will encounter relatively strong decelerating fields as they cross the spaces. Thus, the kinetic energy of the electrons is transferred to the backward wave. The amplitude of the backward wave builds up as it progresses toward the gun end of the tube because it passes more and more bunches from which it receives energy. At the same time, the alternating component of the beam current, i.e., the bunching, becomes greater as the electrons progress through the tube, because of the continuing velocity-modulation interaction between the beam and the established backward wave. See Figure 4-2D.

In the Stewart tube, the rf output signal generated on the helix is coupled off the helix at the gun end on two insulated wires which terminate in an external balun. The balun contains capacitors which block the dc operating potential and allow the output type N connector to operate at chassis ground. These two wires are connected to the end loop of the helix at points 180° apart, to provide balanced output.

In the Varian tube, the rf output signal generated on the helix is coupled off through a dc-blocking capacitor at the gun end, and drives a properly-matched type N or waveguide output connection which operates at chassis ground.

The required cathode-to-helix operating potential is obtained by operating the helix near chassis ground and the cathode below ground. To shift the operating frequency from the low to high band limits, an exponential change of cathode supply voltage over a range of about 7:1 is required. The diode shaper board A13 is factory-set to provide the proper tuning voltage range for each tube, within the supply limits of -150 V to -2500 V.

The anode is operated at a positive potential with respect to the cathode, and the elevated supply board A12 is factory-set to provide feedback that develops the required output within the supply limits of +30 V to +280 V. The grid is operated at cathode potential for full rf output power, and at -27 V (Stewart tubes) or -61 V (Varian tubes) to cut off the electron beam, thereby turning off the rf output completely. This grid control is used for on-off modulation.

Typical cathode current is less than 30 milliamperes, and in Stewart tubes it divides between the helix and collector. In Varian tubes, it divides between the anode and helix, and all beam current not intercepted by the anode flows through the helix lead. In Stewart tubes, the collector is operated at above-ground potential through a resistor from the +100 V supply, and thus is 100 V positive with respect to the helix.

Helix, collector and anode operating currents are critical operating parameters and must not exceed the power dissipating capability of the tube elements. Proper operating current for these electrodes on the particular tube installed in each plug-in unit are indicated on Addendum A enclosed in the envelope bound into the rear of this manual. Because of variations in optimum operating currents and distribution of currents between electrodes in BWO tubes of the same type, the rf power output characteristics should be used as the principal indicator of tube performance.

The rf power output is proportional to the beam current. Beam current is controlled by both grid bias and anode voltage, and maximum beam current is limited by the power dissipating capacity of the helix and anode. Since variations in anode voltage also affect the frequency of oscillation, the sweep oscillator operates with constant anode voltage, and grid bias is used for on-off control of power output. The use of PIN diode leveling for analog power control eliminates the frequency-pulling effect that would be obtained by use of grid modulation to vary the output level. The use of grid control only for on-off control does not cause frequency pulling since the output is cut completely "off" whenever such grid voltage is applied.

In the "standby" mode when the FREQUENCY Mode Selector switch is turned OFF, the heater, cathode, anode, collector, and helix voltages are maintained, and the grid is biased to cut off the beam. With the other voltages applied, the tube is continuously being outgassed, and it has been found that through this technique, tube life is extended by more than 10%.

4.3 TUBE PERFORMANCE MONITORING. Cathode, heater, and anode supply voltages are monitored by means of TP6 through TP9 on the Model 650 chassis, which connect to the supply outputs in that unit. Grid voltage, which is developed in the active stage of the elevated supply PC Board A12 in the plug-in unit, is monitored by TP23 on the left side of the plug-in unit.

Helix current is coupled to terminal 19 of the elevated supply, through the normally-closed contact of FREQ STABILIZING INPUT connector J5. In units having Stewart tubes, this current is shunted to ground through A12R2, a factory selected resistor (nominal value 12.5 - 20 ohms) in the elevated supply in parallel with A7R14, a 49.9-ohm resistor in the helix overcurrent protection circuit (see Figure 4-10 of the Model 650 Instruction Manual). The helix current divides between the two resistors in a suitable ratio, so that a total of between 3.5 and 5 ma helix current, depending upon the tube, is required to produce 1 ma across A7R14 which trips the helix overcurrent relay. The voltage produced by the helix current flow through these resistors and through series resistor A12R15 is monitored at TP22. The monitored voltage will be 100 mv/helix ma, and will be less than 350 - 500 mv, which corresponds to the tripout level. In units having Varian tubes, the helix current is shunted to ground through A12R2 in the elevated supply, in parallel with A7R14, a 49.9-ohm resistor in the helix overcurrent protection circuit (see Figure 4-10 of the Model 650 Instruction Manual). A12R2 is a factory-selected resistor with a nominal value of 1.5-5 ohms. The helix current thus divides between the two resistors, so that a total of between 11 and 34 ma helix current, depending upon the tube, is required to produce sufficient current (about 1 ma) across A7R14 to trip the helix overcurrent relay, which also removes the anode voltage. The voltage produced by the helix current flow through these resistors and through series resistor A12R15 is monitored at TP22. The series resistor is also factory-selected, so that the monitored voltage will be 10 mv/helix ma.

Collector current is coupled to the +100 V supply through a 10-ohm resistor. TP21 is tied directly to the collector, so that the voltage developed by the collector current passing through the resistor will cause the voltage at TP21 to drop from +100 V (TP1) by 10 mv/collector ma.

The +100 V supply input to the elevated supply is not used with Varian tubes. TP21 is non-functional in units with Varian tubes.

4.4 FREQUENCY STABILIZING INPUT.

The FREQUENCY STABILIZING INPUT jack J5 provides a means of applying feedback to the helix to alter the instantaneous helix-to-cathode voltage in single-frequency operation. When generated by a suitable discriminator, such feedback can stabilize the rf single-frequency output to an accuracy approaching that of the best cavity-tuned microwave oscillators. When a mating connector is inserted in J5, it opens the normally-closed contacts shunting R6, so that the applied stabilizing feedback voltage is developed across R6, in series with the helix-to-ground voltage produced by the helix current flow through A12R2 and A7R14. When external stabilization is used, the discriminator voltage applied across R6 ranges between 0 and ±50 V. A positive voltage tends to increase the axial velocity of propagation of the electron beam by increasing the voltage differential between the cathode and helix. This tends to increase the oscillation frequency, in accordance with Figure 4-2A. Conversely, a negative voltage tends to decrease the beam velocity and output frequency. The response characteristics are mainly determined by the external feedback discriminator used to generate the stabilizing signal. The discriminator must have a low output impedance in order to supply the required helix current without affecting the voltage stabilization action.

4.5 DIODE SHAPER BOARD (see Figure 4-3).

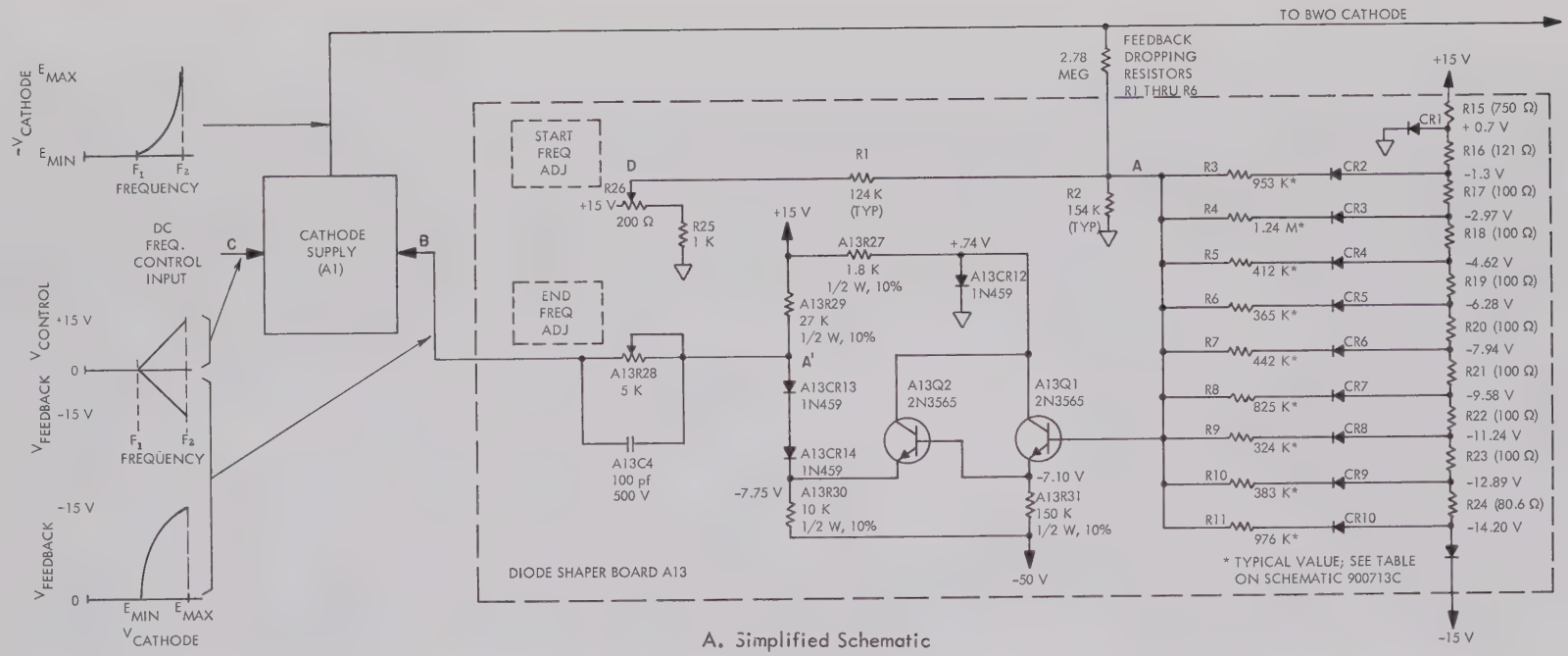
The diode shaper board plugs in to a PC connector in the lower rear compartment of the chassis, accessible from the opening in the left side of the chassis. The diode shaper board provides non-linear feedback as required to produce a tube electron beam velocity which yields frequency output that is linear with sweep time or control voltage. The non-linear relationship of frequency to propagation velocity or electron beam velocity, and the resultant requirement for non-linear helix-to-cathode voltage is evident from Figure 4-2A and B. Since the control voltage is linear with sweep time or sweep frequency, the cancelling feedback must be developed in non-linear fashion, so that a non-linear cathode voltage-vs-frequency curve will result when the feedback is used to balance out a linear control voltage.

The diode shaper board provides this non-linear feedback by shunting away an increasing portion of the feedback current through a biased-diode ladder shunt network as the output level increases. This network is biased by a resistor string connected between + and -15 V, such that only one diode is conducting at the lowest supply voltage, and all diodes are conducting at the highest supply voltage, with a non-linear turn-on curve in between. Thus, an increase in the cathode supply voltage produces a feedback voltage increase at a slower rate (negative slope derivative) as shown in Figure 4-3B. Since the cathode supply circuit increases its voltage output until the control and feedback signals applied to its summing junction cancel, this means that a linear increase in control voltage drives the cathode supply output in non-linear fashion with a positive slope derivative, as shown in Figure 4-3C, to produce feedback cancellation.

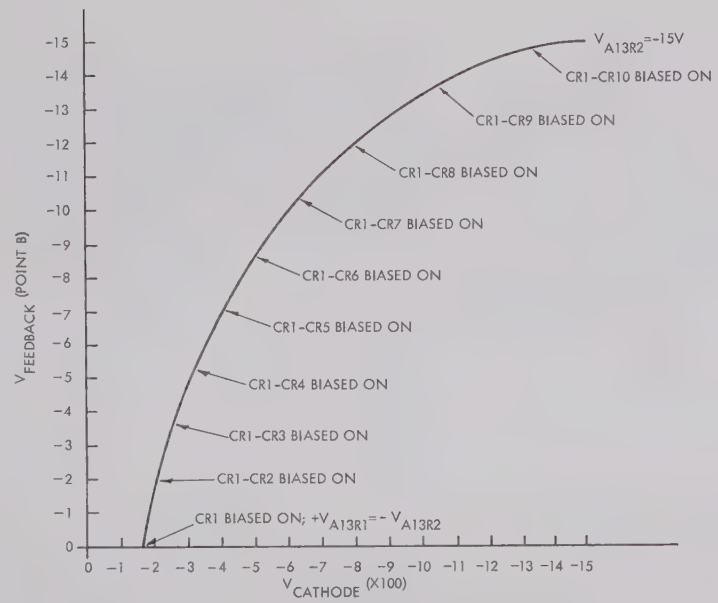
The resultant cathode supply voltage vs. frequency curve produces a linear frequency vs. control voltage performance curve. Mathematically, the curve of required cathode supply voltage vs. frequency (Figure 4-3C) approximates a third-order exponential function. By establishing a feedback curve that corresponds to the second derivative of that curve, a linear feedback voltage-vs-frequency curve is obtained at point B, since the second derivative of a third-order function is linear.

The feedback current supplied to the low-voltage diode shaper board at point A, after passing through dropping resistors R1 through R6, is a fixed fraction of the negative cathode supply output. The feedback voltage developed between point A and ground is produced by the feedback current through A13R2. The ladder network, composed of diodes A13CR1 through A13CR10 and resistors A13R1 through A13R11, achieves the feedback slope shown in Figure 4-3B by progressively shunting a greater percent of feedback current away from A13R2 as the feedback level exceeds the "turn-on" voltage of each biased diode.

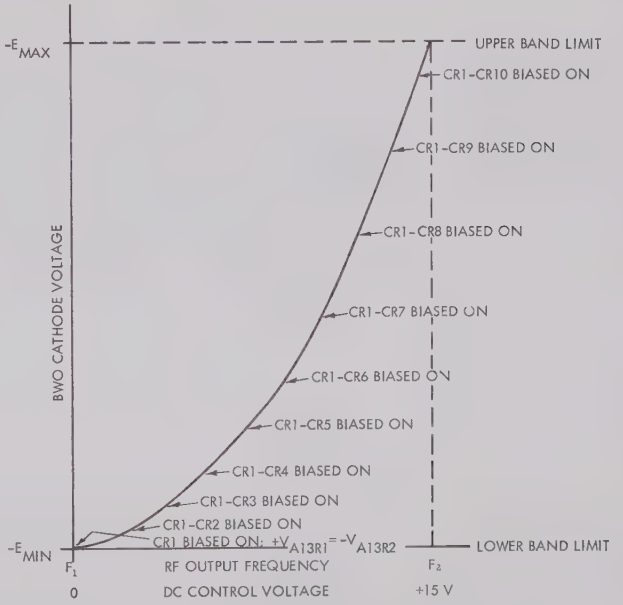
Amplifier stages A13Q1 and A13Q2 are emitter-follower stages that serve as an impedance converter amplifier to provide a low-impedance output at point B with the same voltage as the feedback level at point A. The point B output then serves as a suitable low-impedance source for summing junction feedback current through summing resistors A1R6 and A1R38 in the cathode supply.



A. Simplified Schematic



B. Typical Diode Shaper Feedback Characteristic



C. Typical Overall Voltage vs. Frequency Characteristic for the Feedback-Controlled Cathode Supply

FIGURE 4-3. SIMPLIFIED SCHEMATIC AND OPERATING CHARACTERISTICS OF DIODE SHAPER BOARD

Resistors A13R15 through A13R24 and diode A13CR11 establish the bias on the diode string, and are the same value in all shaper boards. Diode A13CR1 stabilizes the voltage at the junction of A13R15 and A13R16 as the current load changes during feedback shunting conditions. Resistor A13R1 through A13R11 are different for each plug-in unit model, and are selected to obtain the desired curve by shunting away a greater percent of the increasing feedback current as the feedback voltage level reaches the forward-biasing point for each diode. The overall effect is that the feedback voltage developed at point A rises as cathode voltage increases, but at a slower rate, because resistors A13R3 through A13R11 shunt away part of the current that would otherwise pass through A13R2 to develop feedback voltage between point A and ground.

The resultant feedback voltage curve (Figure 4-3B) is a smooth-curve approximation with nine (9) gradual slope changes as the feedback level reaches the turn-on points of A13CR2 through A13CR10. The decrease in slope at each turn-on point is related to the additional shunting effect of the individual resistance values of A13R3 through A13R11 and the net difference between the level at point A and the return voltage of each diode.

At the low band-edge output frequency, the cathode supply output voltage must have a negative value corresponding to the minimum bias requirement of the tube, while the control voltage is zero. Thus, a zero feedback voltage must be developed at a finite supply output level, as shown by the X-axis intercept of the curve in Figure 4-3B. This is accomplished by selecting the value of A13R1 and setting its return voltage (point D) at a positive level between 12.5 and 15 V by means of Start Frequency Adjust resistor A13R26. This positive offset circuit then biases point A positively until the cathode supply voltage increases to the point where the cathode supply feedback voltage supplied to point A equals the positive offset voltage. This establishes a 0-volt null at point A, as required for zero feedback voltage. As the control voltage is subsequently increased above 0 to +15 V, the feedback developed at point A increases non-linearly from 0 to -15 V.

The dc impedance converter amplifier (A13Q1 and A13Q2) consists of two cascaded emitter follower stages with the collectors held close to ground level by A13CR12. Diodes A13CR13 and A13CR14 compensate for the sum of the emitter-base drops in A13Q1 and A13Q2 when these transistors are biased to heavy conduction by a 0-level feedback signal at point A, thus developing a 0-level signal at point A'. As the feedback signal increases to the -15 V level, the conduction of A13Q1 and A13Q2 decreases so that the voltage at A' tracks that at point A, but has a higher source impedance to drive the summing junction.

The End Frequency Adjust resistor A13R28 provides an adjustment for the feedback current supplied from the diode shaper board to summing resistor A1R6 in the cathode supply. It can be used to adjust the overall slope of the diode shaper board curve by establishing the feedback level required to generate 1 ma feedback current to the summing junction at the upper band limit. This then sets the maximum cathode supply voltage limit corresponding to that required by the BWO tube for the upper band-limiting frequency. When the BWO tube is replaced, adjustment of A13R1 and A13R28 permits the feedback of the diode shaper board to be tailored to the characteristic of the new tube without requiring replacement of fixed resistors on the diode shaper board.

4.6 LEVELER AMPLIFIER (see Figure 4-0 and foldout schematic D900933).

4.6.1 GENERAL. The leveler-amplifier is located in the upper compartment of the plug-in unit chassis, near the front. It operates as a closed-loop differential error signal amplifier, driving the PIN diode attenuator assembly to correct for differences between the control and sampler signal levels applied to the differential input stage through A11-6 and A11-7. The output signal is applied to the PIN diode attenuator driver from A11-15, to control both the leveling and matching diodes.

The main leveling loop consists of A11Q1-Q5, A11Q7-Q9, and A11Q11. A11Q1 serves as a differential summing junction, and receives the reference voltage, e_r, from the POWER LEVEL control, and the rf sampler detector output signal, e_s. It develops an error output signal, ϵ_1, which is amplified by differential stage A11Q3/Q4 and single-ended stage A11Q5. The output, e_1, from A11Q5 is combined with the gain-equalizing loop output, e_2, at A11Q7/Q8, which serves as another differential summing junction. The final error signal, ϵ_2, developed by A11Q7/Q8 is amplified by single-ended stage A11Q11 which supplies the control voltage to the leveler driver.

Within the self-balancing leveling system, the PIN diode attenuator can be thought of as a variable-gain attenuator which offers the greatest attenuation in response to the highest sample voltage and the least attenuation in response to the lowest sample voltage, thereby tending to maintain the power output constant at the desired level which corresponds to the POWER LEVEL control setting. The gain equalizing loop functions to improve the performance of the variable-gain leveler attenuator by attempting to hold the error signal amplification within A11 constant, regardless of the power level at which the error occurs. It thereby compensates for the sampler crystal sensitivity variations and the natural gain-bandwidth variation of the amplifier in responding to error signals. Over the full-control range of the leveler (which is a variable attenuation range >15 db and typically 29 db), the gain equalizing loop attempts to maintain a constant effective gain through A_1 of 20 db, so that ϵ_2 tends to be 20 db greater than ϵ_1 regardless of the absolute level of e_s. The gain equalizing loop consists of differential input stage A11Q6, which amplifies the sampler input signal to drive the differential summing junction A11Q7/Q8.

4.6.2 OPERATION OF MAIN LEVELING LOOP AND GAIN-EQUALIZING LOOP IN LEVELED MODE.

Detailed operation of the main loop and gain-equalizing loop is as follows: If the rf output tends to increase to a greater level than that corresponding to the setting of the POWER LEVEL control, the (-) voltage applied to A11-7 from the sampler detector will exceed the reference level applied to A11-6. A11Q1 will become unbalanced, so that the conduction of its right-hand side will be diminished, and the left-hand side will tend to conduct heavily and take control of the stage. The heavier conduction through A11R1, compared with A11R2, will provide greater forward bias to A11Q4, compared to A11Q3, so that A11Q4 conducts more heavily and takes control of the A11Q3/Q4 differential stage. The output from this stage, obtained from the collector of A11Q3, goes negative due to the diminished conduction of A11Q3. This negative-going output tends to reduce the conduction of A11Q5, so that its single-ended output is positive-going. This positive-going signal is applied to the base of A11Q7.

This positive-going voltage change reduces the conduction of A11Q7, producing a negative-going change at the base of A11Q11. The conduction of A11Q11 is thereby increased, drawing more current through A11R37, to drop the leveler control voltage at A11-15. The PIN diode leveler increases its attenuation in response to a drop in control voltage, thus tending to correct for the increase in output power that produced the error signal. Conversely, if the rf output tends to decrease to a level below the setting of the POWER LEVEL control, error signal polarities will be reversed, and the conduction of A11Q11 will be reduced, so that the leveler control voltage becomes less negative, thereby reducing the attenuation. The control signal range at A11-15 is -2 V (maximum attenuation) to -0.5 V (minimum attenuation).

The control signal magnitude produced by the error signal applied to A11Q7 is altered by A11Q8, the other half of the differential stage, which is controlled by the gain equalizing loop amplifier. The negative-polarity sampler signal at A11-7 is applied to the left-hand base of A11Q6 in the gain equalizing amplifier, to provide this control. The sampler signal tends to reduce the conduction of that side of the differential stage by an amount proportional to its magnitude. Since the right-hand base of A11Q6 is connected to ground, this side will always conduct more heavily, but the amount of excess conduction will be proportional to the sampler signal level, i.e. a low-level sampler signal will allow both sides of A11Q6 to conduct almost equally, while a high-level signal will reduce the conduction of the left-hand side, so that the righthand side conducts more heavily. As conduction of A11Q6(R) increases, increased forward bias is applied to A11Q8, causing it to rob current from A11Q7. This reduces the error signal amplification of A11Q7 at high power levels where crystal sensitivity (in terms of Δ db/mv) is greater, and increases it at low power levels where crystal sensitivity is lower. Thus, the overall response of the attenuator (in terms of Δ Z/Δ db) tends to remain constant.

Thus, the positive-going signal at the base of A11Q7 and the negative-going signal at the base of A11Q8 drive differential stage A11Q7/A11Q8 so that A11Q7 conducts less heavily than A11Q8. The decreased conduction of A11Q7 produces a negative-going change at the base of A11Q11.

4.6.3 OPERATION OF PIN DIODE ATTENUATOR AND DRIVER CIRCUIT.

The PIN diode attenuator consists of a pi-network of four shunt diodes and two series diodes connected to form a variable-attenuation transmission line with input and output matching at all attenuation levels. PIN diodes act as electically variable resistors at microwave frequencies, with resistance inversely related to forward bias current in a non-linear manner. Each diode can be varied in resistance over a range of about 10,000 ohms to less than 25 ohms as the applied dc bias current varies from reverse-bias conditions to a maximum forward bias of about 30 ma.

The overall attenuation of the attenuator module ranges from a minimum of ≈ 5 db (max. insertion loss of the structure with the series diodes biased "on" and the shunt diodes biased "off") to a maximum of ≈ 33 db (max. attenuation with the series diodes biased "off" and the shunt diodes biased "on"). In operation the attenuation is controlled over this range by individual signals supplied to each series and shunt diode by the leveler driver circuit, in response to the single control signal developed at A11-15. Thus, a control signal ranging from -0.5 V to -2.0 V from A11 provides full attenuation control from minimum to maximum, respectively.

The attenuator driver circuit and PIN attenuator are factory-adjusted to operate as a single, non-repairable module. The driver circuit includes a control function shaping circuit and separate, factory-adjusted current driver amplifiers for each series and shunt diode. The module has ample design margins in each circuit to assure long life and trouble-free performance. No re-adjustment should be required for the life of the module.

4.6.4 OPERATION IN UNLEVELED MODE.

In the leveled mode, the leveler circuits operate as a closed loop rf level control system with rf sensing and feedback. In the unleveled mode, the system operates as an open-loop rf level control system, and electronic feedback is developed within A11 to close the control signal loop without rf sensing. In the unleveled mode, S2 (C) grounds the sampler signal input so that e_s is zero. This disables the gain equalizing loop and converts the main leveling loop to single-ended rather than differential amplifier operation. To permit accurate adjustment of the power level of the unleveled signal using the POWER LEVEL control R9, amplifier A11Q10 develops a negative feedback signal proportional to the control signal amplitude. This feedback current, combined with the POWER LEVEL control signal current at A11-6, reduces the open-loop gain of the main leveling loop and causes it to operate as a stable dc operational amplifier having gain set by the ratio of A11R31 to R8 (6.67:1). (A11Q10 and A11Q11 are unity-gain emitter followers which provide output isolation and low impedance drive for the feedback and attenuator control outputs.)

In the unleveled mode R9 is driven by a -275 mv signal developed by R3 and R2 from the -15 V supply. The leveler amplifier, operating as a dc operational amplifier, converts the ≈30-275 mv output range of the POWER LEVEL pot R9 into a -2.1 V to -0.4 V attenuator control range, to provide control from maximum to minimum attenuation respectively.

4.6.5 DEVELOPMENT OF REFERENCE LEVEL INPUT SUPPLIED TO A11-6.

4.6.5.1 Leveled Operating Mode. The reference level supplied to A11-6 in the leveled operating mode is the sum of the control signal tapped from the POWER LEVEL potentiometer, R9, and the control voltage "maximum limit" feedback signal developed at A11-17 through A11CR2. Within PC board A11, there is another feedback loop consisting of A11CR1 which supplies the control voltage "minimum limit" feedback signal to the common junction of A11-6 and the left-hand base of A11Q1.

In the leveled mode, the POWER LEVEL pot is driven by the leveler reference output from the modulator (A5-R via P6-8). This signal has a maximum amplitude of about -300 mv corresponding to unmodulated output, and an amplitude of 0 V corresponding to "full off" modulation. Thus, during square-wave or other "on-off" modulation, the leveler reference signal at A11-6 is reduced to 0 V at the same time the rf output drops to 0 (which causes the sampler input at A11-7 to drop to 0), so that the leveler can remain in balance rather than being driven to saturation. With unmodulated output, the variable level applied to A11-6 by adjusting the POWER LEVEL control R9 ranges between 300 mv and a minimum level set by A11R5, which is connected between

R9 and ground. Adjustment of A11R5 sets the minimum level of the output provided by R9 between -60 mv and 0 mv, and the normal setting is selected to provide a power control range of 10 db below the power level at which full-band leveling occurs.

The control voltage "maximum limit" feedback supplied to A11-6 from A11-17 and A11CR2 provides a limit to the unbalance allowed in A11Q1/A11Q2 when the sampler signal amplitude (A11-7) exceeds the reference (A11-6), a condition which tends to cause the (-) A11-15 control voltage to increase in magnitude to increase the attenuation. This feedback thus prevents saturation in A11Q11. A11R42 is adjusted so that A11CR2 clamps the input stage at a level that prevents the A11-15 output from going more negative than -1.95± 0.05 V, which corresponds to a power output 11 db below maximum full-band-leveled power. The principal function of this A11CR2 feedback circuit is to maintain the proper quiescent level of control voltage output at A11-15 when square-wave or other "on-off" amplitude modulation is used. When the modulator reference signal drops to 0 V, there is a slight time lag before the BWO is cut off by the bias signal developed by the transfer oscillator and elevated supply in response to a similar signal from the modulator. During this lag interval, the leveler attempts to cut off the signal by establishing more than the nominal 10 db attenuation. The A11CR2 "maximum limit" feedback circuit restricts this control swing to ≈-1.95 V, to produce about 11 db attenuation, thereby eliminating excessive saturation and the subsequent turn-on lag that such saturation would produce.

The control voltage "minimum limit" feedback applied through A11CR1 to the common junction of A11-6 and the left-hand base of A11Q1 provides feedback when the output level is set above the maximum which allows full-band leveling. This feedback limits the excess error voltage developed during the low power portions of each sweep, to prevent A11Q7 from being saturated in an attempt to reduce the A11-15 output below the level at which minimum signal attenuation is achieved. It sets the minimum output at A11-15 to -0.4 V. By preventing saturation in the amplifier, this feedback provides faster response and a smooth transition between leveled and unleveled portions of a sweep at high power levels where only partial-band leveling is possible.

4.6.5.2 Unleveled Operating Mode. When S2 is in the UNLEVELED position, it removes the "maximum limit" feedback supplied by A11CR2 and applies control feedback from A11Q10 to A11-6 as described in paragraph 4.6.4 above. The POWER LEVEL control is driven by a fixed level of about 270 mv supplied by R3 and R2.

4.6.6 UNLEVELED LIGHT OPERATION. The UNLEVELED WHEN LIT light is connected to +100 V through R4, and glows continually in the unleveled mode, when amplifier A4 is disconnected from the light by S2 (D). In leveled operation S2-D connects the output of amplifier A4 to shunt the lamp-energizing current to ground when leveled operation occurs.

The unleveled light driver amplifier A4 operates as follows: The input stage A11Q12 receives the unleveled sensing signal from A11-20. When operation is continuously leveled over the band being swept, this sensing signal is continuously more negative than -3.75 V. This causes emitter follower A11Q12 to either be biased to cutoff (emitter output = 0 V), or to conduct so slightly that its output is less than +0.6 V. In either case, A11Q13 remains cut off because its base does not receive sufficient bias to produce conduction. With A11Q13 cut off, the A11Q14 base is driven by +15 V through A11R47. A11Q13 then conducts heavily and shunts A11-22 to ground, to extinguish the UNLEVELED light.

When operation is not leveled over the full band being swept, the unleveled sensing signal at A11-20 becomes less negative than -3.75 V over some or all of each sweep interval. These voltage excursions produce sufficient conduction in A11Q12 to base bias of A11Q14 to ground level, cutting it off and discharging capacitor A11C10. A11-20 is then no longer shunted to ground, and the UNLEVELED light is turned on. The time constant of A11R47/C10 and the response characteristic of the unleveled sensing signal are sufficient to prevent A11Q14 from conducting if operation is unleveled over any portion of the cycle. Only when the A11-20 signal is continuously more negative than -3.75 V over the full sweep [indicating leveled operation] will the UNLEVELED light be extinguished.

4.6.7 ADDITIONAL CIRCUIT DETAILS

4.6.7.1 Constant-current Operation of Differential Error Detector/Amplifier Stages. Differential stages A11Q1 and A11Q7/Q8 operate in the common-emitter configuration and in a constant-current mode, driven by A11Q2 and A11Q9, respectively. Constant-current operation is required to provide excellent common-mode rejection, so that 0 V differential output occurs with 0 V differential input.

4.6.7.2 Response shaping. In the Gain Equalizing Loop, the A11C6/A11R21 network provides proper response shaping to provide stable operation. In the Main Leveling Loop, A11C3 provides the basic 6 db/octave response roll-off. The networks A11C4/A11R9 and A11C5/A11R14 provide main loop compensation for the frequency characteristics of the Gain Equalizing Loop. Networks A11C7/A11R27 and A11C8/A11R26 provide additional response shaping and stabilization.

4.7 PIN DIODE ATTENUATOR POWER SUPPLY (See Figure 4-0 and Schematic B900930).

This supply receives 115 Vac from P6 on the plug-in unit chassis. The regulator components are contained on PC board 951087 located in the lower rear compartment of the chassis, just inside and above the opening in the left side of the plug-in unit. The transformer A14T1 and filter capacitor A14C1 are located on the chassis adjacent to the PC board. The 12.6 V output of A14T1 is full-wave rectified by A14CR1 and filtered by A14C1. A14Q1 regulates the output voltage to -12 V, controlled by closed-loop regulator-amplifier A14Q2/A14Q3. The output voltage is sampled by the wiper of A14R6 and applied to the base of A14Q3. The emitter of A14Q3 is held at -4.3 V by zener diode A14CR2. The V_{be} difference voltage thus establishes a low level of conduction in A14Q3. This conduction increases as the output voltage tends to become less negative than -12V. It decreases as the output voltage tends to increase to more than -12 V.

In operation, increased conduction of A14Q3 produces a voltage drop across A14R5 that increases the forward bias of A14Q2. Increased conduction of A14Q2 provides increased forward drive to regulate transistor A14Q1, causing it to conduct more heavily. This produces a negative-going change in the output voltage, to correct for the voltage decrease

that caused the increased conduction of A14Q3. A14R1 and A14R3 establish basic working bias conditions for A14Q1. A14C2 filters the regulated output. The + output lead is connected to chassis ground, and the -12 V is used to supply operating power to the PIN diode attenuator.

4.8 ELEVATED SUPPLY

The elevated supply is located on the front side of the deck that separates the upper front compartment of the chassis from the rear compartment. The passive circuits of the elevated supply PC Board A12 are illustrated and discussed individually in the paragraphs covering related circuits in the main chassis, in Section 4 of the Model 650 Instruction Manual.

The grid modulator A12Q1 is the only active stage in the elevated supply. Operating in the common-base configuration, it acts as an impedance converter and isolator for the output drive current developed by the transfer oscillator driver stage A9Q4.

After passing through A12Q1 the drive current develops the necessary grid bias across A12R5 to operate the grid at a voltage negative with respect to the cathode (0 line). For tube cut-off, a maximum driver/modulator current of 35 ma across A12R5 produces the necessary cut-off bias of -27 V for Stewart tubes at A12-13, and the added drop across A12R4 produces the necessary cut-off bias of -61 V for Varian tubes at A12-14. If a particular Varian tube requires much lower drive voltage, this is achieved by connecting the grid to A12-13 (for -27 V drive) or by interchanging A12R4 and A12R5 and connecting the grid to A12-13 (for -37 V drive). Such changes, when made, are noted on the Addendum A sheet. Diodes A12CR5 and A12CR6 provide a voltage-limiting drop of 30 V from the -100 V supply level to prevent over-voltage conditions and protect A12Q1. Diode A12CR1 prevents the grid from ever being driven positive with respect to the cathode.

4.9 ANODE PROTECTION CIRCUIT

Varian BWO tubes contain a mesh-type intercepting anode (see Figure 4-1B). The power dissipation of this delicate anode electrode must not be allowed to exceed specified ratings. To protect against excess dissipation, an anode protection circuit (A19) Part No. 951619 is installed in plug-in units containing Varian tubes. The anode protection circuit PC board is located in the upper rear compartment of the chassis. This circuit is connected as shown in overall schematic D907053. It completes the connection between A12-4 and A12-5 to couple the anode supply voltage from P7-1 through to the BWO anode. (Plug-in units not containing the anode protection circuit have a shunt wire connecting A12-4 and A12-5 in the elevated supply.)

The anode protection circuit is set to "trip" when the current approaches 90% of the maximum anode current specified by the tube manufacturer. (This is well above the current level required for rated power output.) The Actual Trip-out factory setting is specified in addendum A. When such an anode over-current condition occurs, the anode protection circuit "trips" and connects the anode voltage through a small resistance to the 0-line (which is tied to the BWO cathode). This eliminates the BWO anode-to-cathode positive bias to protect the tube and also triggers a trip-out of the high voltage supply protective circuits. Thus, the trip-out will be indicated by the turn-off of the HIGH VOLTAGE ON lamp on the front panel. After the problem is corrected, operation can be restored by pressing the RESET button.

The anode protection circuit operates as follows (see schematic B907307): The anode current drawn by the anode (A19-3 from the anode supply (A19-1) creates a voltage drop across A19R2, which is partially shunted by its own wiper. A19CR1 provides compensation for the V_{be} drop in sensing transistor A19Q1. The A19R2 wiper is set so that A19Q1 will be turned on by the voltage developed across A19R2 when the desired trip-out current level is reached. As long as A19Q1 is off, there will be approximately equal current through A19R4 and A19R5, so that no forward bias will be applied to A19Q2, keeping it cut off. As long as A19Q2 is cut off, no gate trigger current will be available for silicon-controlled rectifier (SCR) A19Q3, and it will remain gated off.

When A19Q1 senses an overcurrent condition and turns off, the increased current flow through A19R4 provides turn-on bias to A19Q2. A19Q2 turns on and provides gate trigger current to SCR A19Q3. A19Q3 then is gated on and ties the anode supply to the 0-line (and cathode) through A19R7 to immediately shunt the excess current away from the anode. The drop established by this current flow through A19CR2 and A19CR3 biases A19Q4 to conduction. A19Q4 then couples a signal slightly more positive than cathode voltage level to the lamp in A6V1, via P7-2. This energizes the lamp as though it were energized by A6K1 (which would connect the same point to the HTR (+) which is tied to the cathode).

The lamp then triggers the light-sensitive resistor in A6V1 to turn on protection relay K2 which turns off the high voltage supplies in the instrument. Turn-off of the high voltage supplies removes power from the anode protection circuit, so it will automatically reset (if the overcurrent condition is removed) when high voltage is restored by pressing the RESET button or by normal turn-on of the line power switch.

4.10 LOW PASS FILTER (Units with Internal Sampling)

Most plug-in unit models with an internal sampler contain a low-pass filter as the first element in the rf output line from the BWO tube. This filter is a transmission-type resonant structure which attenuates rf harmonics generated by the BWO tube. Plug-in units without an internal sampler (i.e. without the "K" model number suffix) do not contain a low pass filter unless they contain a Varian tube model having an internal filter (see Table 1-1). When external leveling is desired on instruments not having a filter, it may be necessary to add a low-pass filter externally between the RF OUTPUT jack and the external sampler to improve leveling performance. Harmonic output without the filter will be at least 20 db down, but still may generate enough "noise" voltage at the crystal detector to degrade leveling performance at the upper end of some frequency bands.

SECTION 5 - MAINTENANCE

NOTE: Main paragraph topics in this section are numbered to agree with similar paragraphs in the Model 650 Instruction Manual. The material in this section should be used in conjunction with the Maintenance Section (Section 5) of the Model 650 Instruction Manual, which contains material pertinent to all plug-in units. This manual contains supplementary material pertinent to the plug-in "family" covered by this manual.

5.1 PERIODIC MAINTENANCE

Refer to Model 650 Instruction Manual.

5.2 VOLTAGE AND CURRENT MEASUREMENTS AT CHASSIS TEST POINTS.

WARNING

The voltages and current pickoffs pertaining to the plug-in unit, available at test points on the left side of the Model 650 chassis and the plug-in unit, are at high-voltage levels. Suitable high-voltage measurement techniques and precautions must be used. The cathode supply output (TP6) is measured with respect to chassis ground, and a high-voltage voltmeter or a high-voltage probe must be used. For all other high voltage measurements, the "0 line" (TP10) which is at or near cathode potential is the reference rather than chassis ground, and a floating voltmeter with at least 3000 volts insulation must be used. The heater supply output between TP7 and TP8 may be measured with high voltage off, using a standard meter. The lowest cathode supply output voltage, which in some plug-in units is low enough for measurement without an insulated meter, is obtained with a single frequency setting at the low end of the band. Other high voltage measurements should be made at this setting of the cathode supply to keep the "0 line" voltage as low as possible for safety reasons.

a. Cathode Supply Voltage. [TP6 (-) to chassis ground (+)]. The proper cathode supply output voltages for the lower and upper band limit frequencies, corresponding to single-frequency settings of 0 and 100 on the logging scale, are given in Addendum A in the envelope at the rear of this manual. When a replacement BWO tube has been installed, refer instead to the manufacturer's data sheet for the tube, which should be in the same envelope. Make the dc voltage measurement with a high voltage digital meter or a suitable voltage-divider probe. Refer to Paragraph 5.3.4 of this manual for measurement of ripple effects.

b. Heater Voltage. [TP7 (+) to TP8 (-)]. All the BWO tubes utilize less than 1.5 amperes heater current, so that the proper heater voltage with one of these plug-in units loading the supply is 6.3 V ±0.1 V. It is recommended that this voltage be measured with the high voltage turned off by removing the top or rear panel to defeat the interlock switch. If the measurement is made with high voltage on, the heater will be at cathode potential, and a floating insulated voltmeter must be used.

c. -100 V Supply. [TP10 (+) to TP11 (-)]. Voltage should be 100 ± 0.1 V. For this measurement, the instrument must be operating with the FREQUENCY Mode Selector switch set to SINGLE, and with no modulation applied. The measurement must be made using a floating, insulated voltmeter.

d. Anode Supply. [TP9 (+) to TP10 (-)]. The proper anode supply voltage output setting is given in Addendum A in the envelope at the rear of this manual. When a replacement BWO tube has been installed, refer instead to the manufacturer's data sheet for the tube, which should be in the same envelope. The measurement must be made using a floating, insulated voltmeter.

e. Grid Voltage. [TP10 (+) to TP23 (-)]. Voltage should be ≈0 V with controls set for RF ON (unmodulated); and 61 ± 10 V (for Varian BWO tubes; less if noted on Addendum A) or 27 ± 2 V (for Stewart or WJ BWO tubes) with the FREQUENCY Mode Selector switch in the OFF position. The measurement must be made using a floating, insulated voltmeter.

f. Collector Current.

Instruments Having Varian BWO tubes: Collector current cannot be measured on Varian BWO tubes. All beam current is intercepted by the helix. The "Collector Current" test point (TP21) on these plug-in units is non-functional.

Instruments Having Stewart (W-J) BWO tubes: [TP1 (+) to TP21 (-)]. The proper collector currents in ma for the lower and upper band limit frequencies, corresponding to single-frequency settings of 0, 50, and 100 on the logging scale, are given in Addendum A in the envelope at the rear of this manual. When a replacement BWO tube has been installed, refer instead to the manufacturer's data sheet for the tube, which should be in the same envelope.

To measure the collector current, measure the voltage drop between TP1 (+) and TP21 (-). This is the voltage drop produced across a 10-ohm resistor by the collector current. Hence, the voltage measured in millivolts must be divided by 10 to obtain the current in milliamperes.

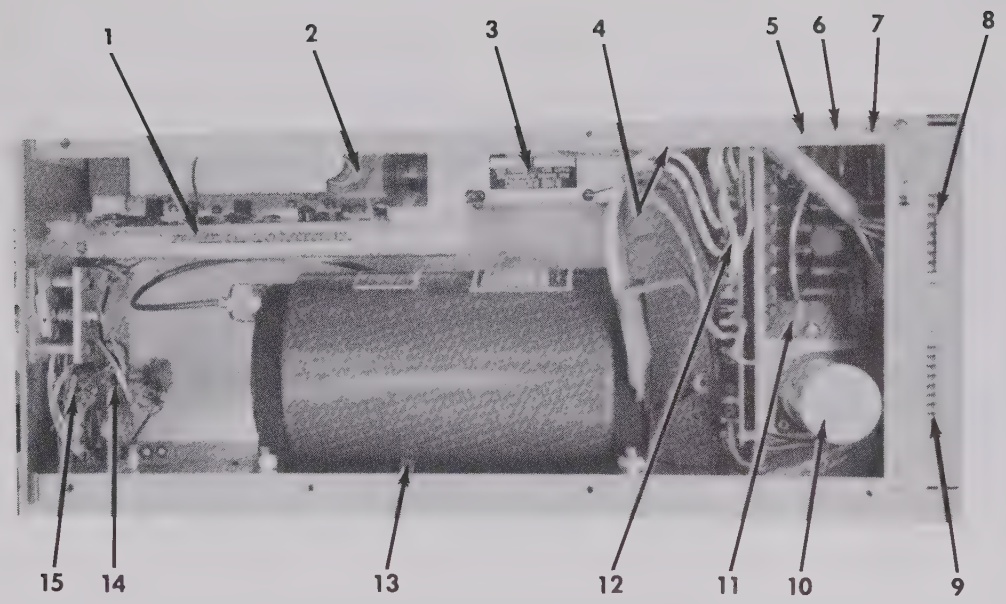
CAUTION

This voltage measurement is at the level of +100 V, since TP1 is the +100 V test point. Thus, the voltmeter used must be floating, but need not be a high-voltage unit.

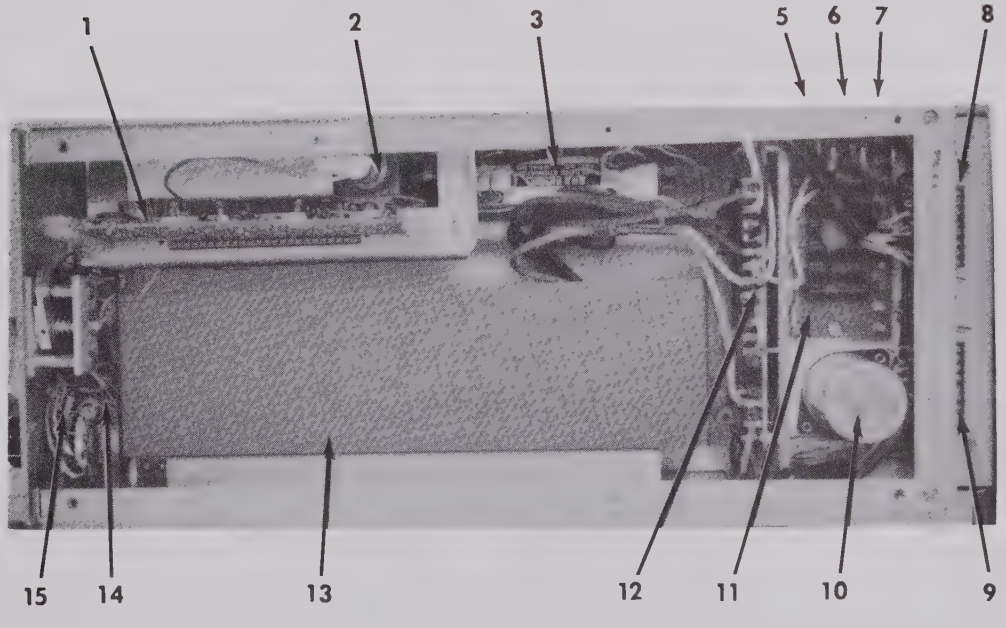
g. Helix Current. [TP22 (-), TP21 or chassis ground (+)]. The proper helix currents in ma for the lower and upper band limit frequencies, corresponding to single-frequency settings of 0 and 100 on the logging scale, are given in Addendum A in the envelope at the rear of this manual. When a replacement tube has been installed, refer instead to the data sheet for the new tube, which should be in the same envelope.

NOTE: The helix overcurrent trip-out level is listed in Addendum A.

Instruments Having Varian BWO tubes: To measure the helix current, measure the voltage drop between TP22 and TP21 (or chassis ground) with the controls set for



A. Plug-in unit having Varian BWO Tube

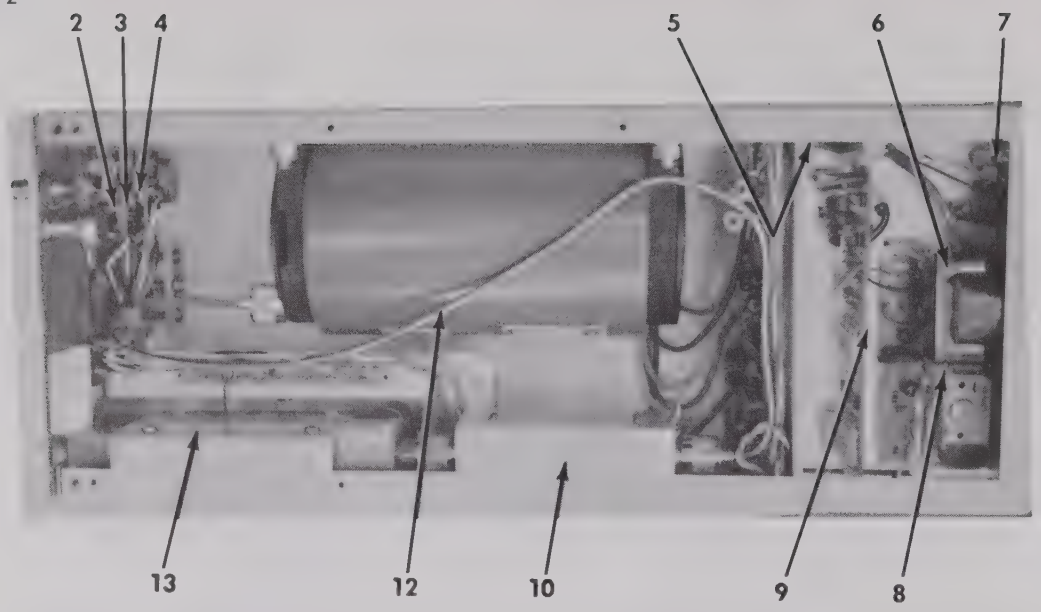


B. Plug-in unit having Stewart or WJ BWO Tube

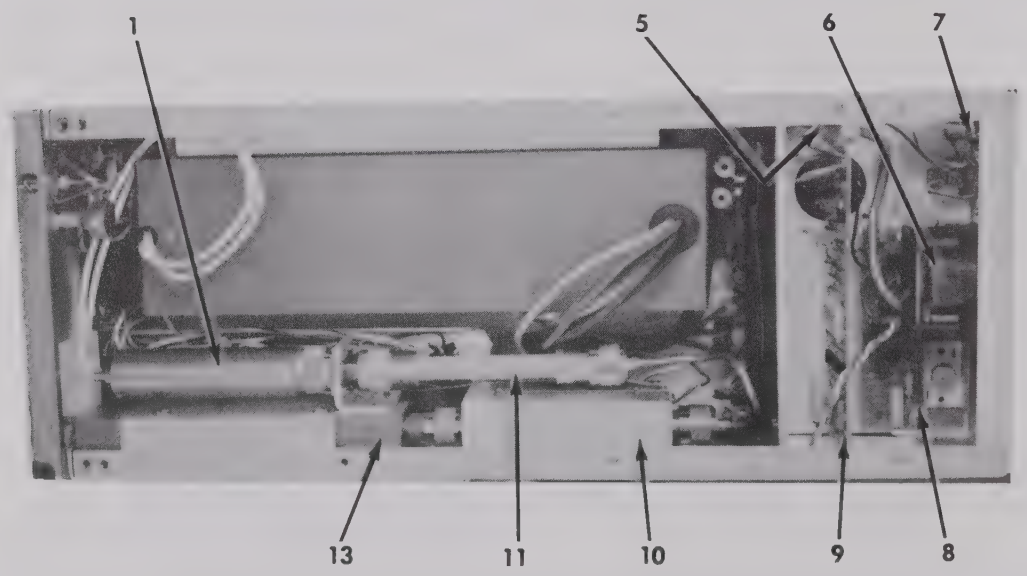
LEGEND FOR FIGURE 5-1

1. LEVELER-AMPLIFIER PC BOARD A11
2. INTERNAL SAMPLER (Consists of directional coupler + crystal detector; provided in plug-in units having K suffix only)
3. PIN DIODE ATTENUATOR MODULE (Assembly of PIN Diode Attenuator and PIN Diode Driver, which is non-adjustable and non-repairable in the field)
4. ANODE PROTECTION CIRCUIT PC BOARD A19 (Used with Varian BWO tubes – which have intercepting type anode-only; factory-set for trip-out at maximum anode current allowed for the tube type used)
5. TP23 (GRID)
6. TP22 (HELIX)
7. TP23 (COLLECTOR) (Non-functional in units with Varian BWO tubes)
8. P7
9. P6
10. A14C1
11. HIGH VOLTAGE FEEDBACK BOARD A25
12. ELEVATED SUPPLY PC BOARD A12
13. BWO TUBE
14. R8
15. R4

Figure 5-1. Top View of Plug-in Unit



A. Plug-in unit having Varian BWO Tube



B. Plug-in unit having Stewart or WJ BWO Tube

LEGEND FOR FIGURE 5-2

1. BWO TUBE OUTPUT BALUN (Part of Stewart/WJ BWO tube assy; serves as tube RF output connection; not used on Varian tubes.)
2. R2 } Same location in both plug-in
3. R3 } units; visible here only on
4. R1 } photo of unit containing Varian BWO
5. TIME METER M1
6. A14T1
7. R6
8. PIN DIODE ATTENUATOR POWER SUPPLY A14
9. DIODE SHAPER PC BOARD A13
10. PIN DIODE ATTENUATOR MODULE (Assembly of PIN Diode Attenuator and PIN Diode Driver, which is non-adjustable and non-repairable in the field)
11. OUTPUT LOW-PASS FILTER (Used only in Plug-in units having an internal sampler when the BWO tube structure does not contain an internal low pass filter)
12. BWO-TO-PIN ATTENUATOR MINIATURE COAX CONNECTION (Varian BWO tube shown contains an internal low-pass filter; when the tube does not contain an internal filter, an external filter is installed at this point in the rf circuit on plug-in units having internal sampling)
13. INTERNAL SAMPLER (Consists of directional coupler + crystal detector; provided in plug-in units having K suffix only)

Figure 5-2. Bottom View of Plug-in Unit

RF ON (unmodulated). This is the voltage drop produced across a 10-ohm resistance by the helix current. Hence, the voltages measured in millivolts must be divided by 10 to obtain the current in milliamperes. This voltage measurement is at ground level so that a floating or high voltage voltmeter need not be used.

Instruments Having Stewart (WJ) BWO Tubes. To measure the helix current, measure the voltage drop between TP22 and TP3 (or chassis ground). This is the voltage drop produced across a 100 Ω resistance by the helix current. Hence, the voltage measured in millivolts must be divided by 100 to obtain the current in milliamperes. This voltage measurement is at ground level so that a floating or higher-voltage voltmeter need not be used.

h. Anode Current. (To be measured only on instruments having Varian BWO Tubes.) The maximum allowable anode current and voltage are listed in Addendum A. On units having an anode protection circuit, the current trip-out level is indicated on Addendum A. Operating current level is typically well below the maximum value. To measure the anode current, use a clip-on milliammeter to monitor current in the red lead which connects to terminal 5 of the Elevated Supply PC Board A12.

WARNING

It will be necessary to remove the instrument top cover and "cheat" the interlock switch to operate the unit with access to the anode lead. Avoid touching the exposed live circuits.

5.3 INTERNAL ADJUSTMENTS.

5.3.1 AVAILABLE ADJUSTMENTS. There are six field-adjustable internal adjustments in each plug-in unit. The instrument is designed for long-term operation with a minimum of adjustment. The main purpose of the adjustments provided is to simplify the recalibration of the plug-in unit after installation of a new BWO tube or other components. These adjustments can also be used for periodic recalibration at six-month intervals or as required. The adjustments and a recommended adjustment procedure for each, are described in the paragraph below. These procedures permit the plug-in unit to be calibrated to meet published specifications, when operating in a Model 650 main chassis which has been factory calibrated or field-calibrated with a Sweep Oscillator Calibration Plug-in unit, Model 650T.

A factory calibration service is available at nominal cost by returning the instrument to ALFRED. Contact your nearest representative or our sales department.

NOTE: Refer to the Model 650 Instruction Manual for adjustment procedures for the heater supply voltage (A6R21 HEATER ADJ., Paragraph 5.3.3), the -100 V supply voltage (A6R5 -100 V ADJ., Paragraph 5.3.3). These adjustments are accessible from the top of the Model 650 chassis, as shown in Figure 5-1 of the Model 650 Instruction Manual.

5.3.2 ADJUSTMENT PROCEDURES. (The adjustments are shown in Figure 5-2 of the Model 650 Instruction Manual and Figure 5-1 of this manual.)

CAUTION

Use a non-metallic screwdriver in making these adjustments. Use of a metallic screwdriver can

(1) Damage magnetic focus of microwave tube
(2) Cause circuit-damaging shorts in the area between the access opening and the adjustment screw.

1. A12R9 ANODE ADJUST Adjustment. This control is used for fine adjustment of the anode voltage supplied to the BWO tube, after installation of the proper-value resistor in the A12R14 circuit position. Re-adjustment will usually be required when the BWO tube is replaced, and may also be useful to restore the power output level of an aging BWO tube. The final criteria of a proper anode voltage adjustment (assuming other voltages are correct) is that rated power output is obtained. Thus, the final adjustment of anode voltage should be made in terms of output power, in accordance with the procedure of Par. 5.3.5, rather than setting the voltage to a specific value. For a newly installed BWO, a preliminary adjustment of anode voltage to the operating value specified by the manufacturer can be made using the following procedure. To check anode voltage in older tubes, use the procedure of paragraph 5.3.5.

   a. Determine the operating value for anode voltage specified in Addendum A (located in the envelope at the rear of this manual). Use this voltage value and the graph given in Figure 5-8 of the Model 650 Instruction Manual to determine the correct value for feedback resistor A12R14 at the top of the elevated supply PC board in the plug-in unit.

   b. If the value of A12R14 determined in Step a differs from the resistor actually installed in the unit, change A12R14 to the proper value.

   c. Measure the anode voltage between TP9 (+) and TP10 (-), and adjust it to the value determined in Step a, by using A12R9 ANODE ADJ.

   WARNING

   This is a high-voltage measurement. Use a floating, insulated voltmeter.

   d. Refer to Paragraph 5.3.5 for a final adjustment of anode voltage in terms of output power.

2.,3. A13R26 START FREQ and A13R28 END FREQ. Adjustments. These controls are used to set the cathode supply feedback limits to obtain the proper cathode supply output voltage limits for the BWO tube, corresponding to the lower and upper frequency limits of the plug-in unit. The proper voltages for these band limits can be checked as described in Paragraph 5.2 a above.

   Adjustment of these voltages to the prescribed values is the first step in frequency calibration of the plug-in unit. Final frequency calibration involves a further adjustment of A13R26 and A13R28 to obtain calibrated frequency performance. Once final frequency calibration of the plug-in unit has been carried out using the procedure given in Paragraph 5.3.3, these voltages should not be re-adjusted.

   Refer to Paragraph 5.3.3 for the proper adjustment procedure.

4. A11R17 INTERNAL SAMPLER SENSITIVITY Adjustment (functional on instrument models with K-suffix numbers only). This control is used to set the signal level applied to the leveler-amplifier from the internal rf sampler. The proper adjustment for standard applications is one that places the full-band leveling point at about the center of the adjustment

range of the red POWER LEVEL control, and provides unleveled output with the POWER LEVEL control turned fully CW. This allows the POWER LEVEL control to be set above the full-band-leveled position for higher-level, partial-band leveling, and also provides an adjustment range of at least 10 db below full-band-leveled power when the POWER LEVEL control is turned fully CCW. Note that this control affects only operation with the internal rf sampler. When an external sampler is used, the front panel RF SAMPLER SENS. control provides a similar adjustment function. To achieve the maximum available amount of controlled reduction below the full-band-leveled point, a readjustment of the MINIMUM POWER LIMIT control A11R5 may also be required.

The adjustment procedure is as follows:

a. Connect the RF OUTPUT jack through a suitable attenuator to a crystal detector, and connect the detector output to the vertical channel of an oscilloscope. Use the OUTPUT SWEEP signal to drive the horizontal channel of the oscilloscope.

b. Make the following control settings:

RF OUTPUT to LEVELED
RF SAMPLER to INT
FREQUENCY Mode Selector to SWEEP $F_1 \rightarrow F_2$
MARKER AMPLITUDE to OFF
MODULATION to AM OFF
SWEEP TIME to .01
SWEEP TRIGGER to FREE RUN
F_1 to 0 on the logging scale
F_2 to 100 on the logging scale
Red POWER LEVEL fully CW.

c. Adjust the INTERNAL SAMPLER SENS adjustment until the maximum-power excursion of the unleveled CRT presentation appears flattened, indicating the start of leveled response. Then back off the adjustment so that the unleveled trace remains unaffected with the red POWER LEVEL control remaining fully CW.

d. Turn the red POWER LEVEL control CCW slowly and observe the onset of leveled performance across the band. Full-band leveling should be achieved at a mid-range setting of the POWER LEVEL control.

e. With the red POWER LEVEL control set at the maximum power level that allows full-band-leveling, adjust the oscilloscope vertical amplification to obtain a full-height presentation on the CRT.

f. Turn the red POWER LEVEL control fully CCW. With the oscilloscope amplification set as above, and the input to the crystal detector attenuated sufficiently that square-law operation is assured, the presentation on the CRT should be reduced to less than 1/10 of the amplitude observed in Step e. If not, adjust the MINIMUM POWER LIMIT adjustment A11R5 to achieve such a reduction in level. If the adjustment of A11R5 seems to "bottom out" with insufficient range, turn the CONTROL VOLTAGE LIMIT adjustment A11R42 fully CW to permit adjustment of the MINIMUM POWER LIMIT. Then refer to item 6 below to properly adjust the CONTROL VOLTAGE LIMIT.

5. A11R5 MINIMUM POWER LIMIT Adjustment. This control is used to set the range of the red POWER LEVEL control by adjusting its lower limit. It does not affect the upper limit of the control range.

The adjustment procedure for units with internal sampling is included as the final step in the INTERNAL SAMPLER SENS adjustment procedure, item 4 above. For units without internal sampling, use the following procedure:

a. Connect a suitable external sampler loop, consisting of a sampler or a directional coupler and detector, to the RF OUTPUT jack, and use the sampler output to drive the RF SAMPLER INPUT jack (see Figure 5-3). Terminate the rf loop with a suitable attenuator and crystal detector, and connect the detector output to the vertical channel of an oscilloscope. Use the OUTPUT SWEEP signal to drive the horizontal channel of the oscilloscope.

b. Make the following control settings:
RF OUTPUT to LEVELED
RF SAMPLER to EXT
FREQUENCY Mode Selector to SWEEP $F_1 \rightarrow F_2$
MARKER AMPLITUDE to OFF
MODULATION to AM OFF
SWEEP TIME to .01
F_1 to 0 on the logging scale
F_2 to 100 on the logging scale
Red POWER LEVEL fully CW

c. Adjust the front panel RF SAMPLER SENS control until the maximum-power excursion of the unleveled CRT presentation appears flattened, indicating the start of leveled response. Then back off the adjustment so that the unleveled trace remains unaffected with the red POWER LEVEL control remaining fully CW.

d. Turn the red POWER LEVEL control CCW slowly and observe the onset of leveled performance across the band. Full-band leveling should be achieved at a mid-range setting of the red POWER LEVEL control.

e. With the red POWER LEVEL control set at the maximum power level that allows full-band-leveling, adjust the oscilloscope vertical amplification to obtain a full-height presentation on the CRT.

f. Turn the red POWER LEVEL control fully CCW. With the oscilloscope amplification set as above, and the input to the crystal detector attenuated sufficiently that square-law operation is assured, the presentation on the CRT should be reduced to less than 1/10 of the amplitude observed in Step e. If not, adjust the MINIMUM POWER LIMIT adjustment A11R5 to achieve such a reduction in level.

If the adjustment of A11R5 seems to "bottom out" with insufficient range, turn the CONTROL VOLTAGE LIMIT adjustment A11R42 fully CW to pemit adjustment of the MINIMUM POWER LIMIT. Then refer to item 6 below to properly adjust the CONTROL VOLTAGE LIMIT.

6. A11R42 CONTROL VOLTAGE LIMIT Adjustment. This adjustment is used to set the quiescent leveler attenuation whenever rf output is modulated off, by setting the limiting amount of controlling feedback supplied in the absence of rf signal. The operating characteristics do not change with age. Unless the factory adjustment is altered improperly, readjustment should not be required unless the PIN attenuator or A11Q1 are replaced. At that time, A11R42 should be carefully set to provide a proper balance between the extremes of (1) producing excess overshoot in square wave modulation, and (2) interfering with smooth leveling at low power levels.

To adjust the CONTROL VOLTAGE LIMIT, proceed as follows after having set the MINIMUM POWER LIMIT in accordance with item 5 above:

a. With the plug-in unit installed in a Model 650, make the following control settings:

RF OUTPUT to LEVELED
FREQUENCY Mode Selector to SWEEP $F_1 \rightarrow F_2$
MARKER AMPLITUDE to OFF
MODULATION to AM OFF
SWEEP TIME to .1
SWEEP TRIGGER to FREE RUN
F_1 to 0 on the logging scale
F_2 to 100 on the logging scale
POWER LEVEL (red) fully CCW.

b. If the unit has internal sampling, switch the RF SAMPLER INT-EXT switch to INT. For units without internal sampling, obtain leveled operation with an external sampler as described in item 5a above.

c. Connect the RF OUTPUT jack through a suitable attenuator to a crystal detector, and connect the detector output to the vertical channel of an oscilloscope. Use the OUTPUT SWEEP signal to drive the horizontal channel of the oscilloscope.

d. Rotate the POWER LEVEL control slightly away from the CCW position and then back to the CCW position, and observe the oscilloscope trace level which should move up and down to indicate that the output level is tracking the setting of the POWER LEVEL control. (If this response is not obtained, turn the CONTROL VOLTAGE LIMIT adjustment A11R42 in the CW direction to permit such response).

e. While continuing to rotate the POWER LEVEL control as in step d, turn the CONTROL VOLTAGE LIMIT adjustment A11R42 CCW slowly until it interferes with smooth tracking, by the oscilloscope trace, of the POWER LEVEL control. When the point of interference is reached, back off the CONTROL VOLTAGE LIMIT adjustment in the CW direction about 3 degrees.

f. Turn the MODULATION Mode Selector to SQ WAVE.

g. Observe the character of the square wave pulses and make a slight "touch-up" adjustment of the CONTROL VOLTAGE LIMIT adjustment A11R42 over a range of $\pm 3^{o}$ to improve pulse rise time [ccw adjustment] and limit overshoot [cw adjustment] if necessary.

5.3.3 FREQUENCY CALIBRATION ADJUSTMENT. (See Figure 5-3.)

The following procedure should be used for periodic frequency calibration of the plug-in unit.

CAUTION

Check all supply voltages except the cathode supply output before performing this calibration adjustment. (See Paragraph 5.2.)

a. Let the instrument warm up and stabilize for about 1/2 hour.

b. Make the following control settings:

RF OUTPUT to UNLEVELED
FREQUENCY Mode Selector to SINGLE
SINGLE Frequency Selector to F_o
MARKER AMPLITUDE to OFF
MODULATION to AM OFF
SWEEP TIME to .01
SWEEP TRIGGER to FREE RUN

c. With the instrument operating unmodulated in single-frequency mode, measure the cathode supply voltage output at the lower and upper band limit frequencies as directed in Paragraph 5.2 a.

d. If either voltage measured in Step c is incorrect, adjust it to the proper value using the START FREQ or END FREQ adjustment controls. (The START FREQ adjustment sets the voltage at the low band-edge frequency, and the END FREQ adjustment sets the high band-edge frequency.)

e. Connect an rf test loop consisting of a variable attenuator, wavemeter, and crystal detector to the RF OUTPUT jack, and connect the crystal detector output to the vertical channel input of an oscilloscope. Use the OUTPUT SWEEP signal to drive the horizontal channel of the oscilloscope.

f. Make the following changes in control settings:

FREQUENCY Mode Selector to SWEEP $F_0 \pm \Delta F$
RETRACE to OFF
ΔF AMPLITUDE fully CW
SWEEP TIME to .01
SWEEP TRIGGER to FREE RUN
F_0 to 0 on the logging scale (the low band-edge frequency).

g. Adjust the wavemeter to the low band-edge dial frequency.

h. Observe the CRT presentation. The wavemeter pip should appear exactly in the center of the trace. If it is obviously off-center, adjust the A13R26 START FREQ control to move the pip to the center of the trace.

i. When the pip appears centered in Step h above, turn the ΔF AMPLITUDE control CCW slowly, continuing to the CCW rotation limit (labeled 0) unless the pip appears off center as the control is turned. If necessary, stop turning the ΔF AMPLITUDE control, and adjust the START FREQ control again to restore the pip to the center of the trace. Continue this procedure until the ΔF AMPLITUDE control is fully CCW and the wavemeter pip remains in the center of the trace.

k. Adjust the F_0 FREQUENCY control to 100 on the logging scale, and turn the ΔF AMPLITUDE control fully CW.

l. Observe the CRT presentation. The wavemeter pip should appear exactly in the center of the trace. If it is obviously off-center, adjust the A13R28 END FREQ control to move the pip to the center of the trace.

m. When the pip appears centered in Step l above, turn the ΔF AMPLITUDE control CCW slowly, continuing to the CCW rotation limit (labeled 0) unless the pip appears off center as the control is turned. If necessary, stop turning the ΔF AMPLITUDE control, and adjust the END FREQ control again, to restore the pip to the center of the trace. Continue this

procedure until the Δ F AMPLITUDE control is fully CCW and the wavemeter pip remains in the center of the trace.

n. Adjust the F_0 FREQUENCY control to 0 on the logging scale and turn the Δ F AMPLITUDE control fully CW.

o. Repeat Steps g through m .

p. Repeat Steps n and o one additional time. Repeating the steps is necessary because of the interaction of the START FREQ adjustment A13R26 and END FREQ adjustment A13R28.

q. Check the frequency tracking across the dial at several points by turning the Δ F AMPLITUDE control to 100% and setting both the F_0 control and the wavemeter to identical frequencies. Frequency accuracy should be ± 1.0% or better across the band. If necessary, Steps n through p above may be repeated to offset the end frequencies slightly in order to reduce mid-band inaccuracies.

r. If it is desired to check residual FM and AM, retain the test set-up and follow the procedures of Paragraph 5.3.4.

5.3.4 CHECKOUT PROCEDURE FOR RESIDUAL FM AND AM. (See Figure 5-3.)

The following procedure may be used for periodic verification that residual FM and AM are within specification limits. It should be performed as a continuation of the frequency calibration adjustment procedure given in Paragraph 5.3.3 above. The FM procedure should be performed at the low-frequency end of the band, because residual FM effects are greatest at that point; and the AM procedure should be performed at the high-frequency end of the band.

a. The instrument should be operating with an rf test loop equivalent to Step e of Paragraph 5.3.3. To permit the high sensitivity readings required later in the procedure, the oscilloscope should be set for AC coupling.

b. Make control settings as in Step f of Paragraph 5.3.3, except turn the RETRACE control to ON. Also set the RF OUTPUT control to UNLEVELED and turn the MARKER AMPLITUDE and MODULATION both off.

c. Adjust the wavemeter to center its pip on the CRT display.

d. Slowly turn the Δ F AMPLITUDE control CCW, while adjusting the wavemeter slightly, as necessary to keep the wavemeter pip centered on the CRT trace. Continue this procedure until the Δ F AMPLITUDE control is fully CCW.

e. Set the wavemeter to place one-half of its pip at the left edge of the trace.

f. Adjust the oscilloscope horizontal sensitivity to yield a full-graticule sweep width while being driven by the OUTPUT SWEEP signal of the Model 650.

g. Adjust the oscilloscope for maximum vertical sensitivity, and set the vertical position so that the CRT presentation shows a sloping line which represents one half of the magnified wavemeter pip at the left edge.

h. Adjust the wavemeter enough to move the sloping line (pip) from one edge of the CRT presentation to the other. The difference between the wavemeter settings at each edge of the CRT will calibrate the horizontal scale (typical value = 20 MHz).

NOTE: Since the rf output is being left on during retrace, there will be two parallel lines visible on the CRT. Use the brighter line for the residual FM check (the dim line is the retrace indication).

i. Adjust the wavemeter slightly to move the sloping line to a convenient point on the graticule.

j. Observe the horizontal jitter in the line while the Δ F AMPLITUDE control remains fully CCW. The horizontal scale calibration made in Step h above will permit evaluating the frequency deviation of the jitter.

This deviation should be less than ± the KHz value indicated for "Maximum Residual FM" in Table 1-1. If excess residual FM is present, check the ripple voltage on the cathode supply [TP6 (-), TP3 (+; chassis ground)] . Use an oscilloscope with a 10:1 divider probe and operate the vertical channel with ac coupling. The ripple voltage should not exceed ± 0.01% of the cathode supply output voltage at any point. Refer to the trouble-shooting procedures for the cathode supply in the Model 650 Instruction Manual if excess ripple is present. If excess power supply ripple is not present, the excess residual FM may be due to arcing or other breakdown in the BWO tube, which requires replacement of the tube.

NOTE: If desired, the above residual FM checkout procedure may be repeated at other frequencies in the frequency range of the plug-in unit. Since arcing or other breakdown effects in a BWO tube often show up initially as excess residual FM, this procedure can be useful in evaluating performance of aging BWO tubes.

k. Make the following changes in control setting, in preparation for the residual AM test.

FREQUENCY Mode Selector to SINGLE
SINGLE Frequency Control to F_0
F_0 control set at high end of band
MODULATION to SQUARE WAVE

l. Adjust the wavemeter away from the frequency of interest, or remove it from the circuit completely, as desired. Make sure that sufficient attenuation is provided in the rf test loop to allow the crystal detector to operate in its square-law range.

m. Adjust the oscilloscope for a free-running horizontal sweep independent of the Model 650. Leave the oscilloscope vertical input ac-coupled. Adjust the oscilloscope vertical sensitivity to obtain a square-wave height of 5 scale divisions (5 cm) on the graticule.

n. Turn the MODULATION switch to AM OFF.

o. Without changing the vertical sensitivity vernier adjustment of the oscilloscope from the setting used to obtain a full-height display in Step m, set the oscilloscope vertical sensitivity switch to step-increase vertical amplification by a factor of 10 (so that the full-height square-wave presentation would now be 50 scale divisions). Adjust the zero position control as necessary to place the unmodulated power-on straight line trace at a convenient line on the graticule.

p. Observe the vertical ripple on the CRT trace. It must not exceed 1 scale division (i.e. less than 1/50 or 2% of the output amplitude). If excess residual AM is present, refer to the trouble-shooting procedures for the anode and -100 V supplies in the Model 650 Instruction Manual.

5.3.5 POWER CALIBRATION ADJUSTMENT. (See Figure 5-4.) The following adjustment procedure can be used to check and adjust the power output level of the plug-in unit.

a. For newly installed BWO tubes, check and adjust the anode voltage to the specified level using the ANODE ADJ control as directed in Paragraph 5.3.2. For adjustment of older tubes, proceed as follows:

b. Connect an rf test loop consisting of a suitable attenuator pad, a thermistor mount, and a power meter to the RF OUTPUT jack to measure absolute power. Be sure to use a large enough attenuator to avoid exceeding the thermistor power rating. Alternately, an ALFRED 8000/7051 Oscilloscope/Sweep Network Analyzer system can be used for making direct absolute power measurements with a crystal detector.

c. Make the following control settings:

RF OUTPUT to UNLEVELED
FREQUENCY Mode Selector to SWEEP $F_1 \rightarrow F_2$
MARKER AMPLITUDE to OFF
MODULATION to AM OFF
SWEEP TIME to 10
SWEEP TRIGGER to FREE RUN
F_1 to 0 on the logging scale
F_2 to 100 on the logging scale

d. Observe the unleveled power output characteristic. If power at any point in the sweep is below the level specified in Table 1-1, increase the anode voltage slightly using the ANODE ADJ control.

NOTE: The ANODE ADJ. control permits an adjustment range of about ± 10 V. To provide a greater adjustment, particularly to restore the tube power output level after long service, it may be necessary to raise the anode voltage by more than the amount available from the ANODE ADJ. control. To do this, substitute a slightly higher resistance value for A12R14 (see Figure 5-8 in the Model 650 Instruction Manual), and then set the ANODE ADJ. control.

WARNING

Turn power off when changing A12R14.

CAUTION

Do not increase the anode supply voltage more than necessary to bring the minimum power point up to specifications. If rated power output cannot be obtained with a reasonable anode voltage, and other tube operating voltages are correct, the BWO tube must be replaced. Also, helix current must not be allowed to exceed the specified maximum (trip out) level. Any increase in anode voltage over the factory setting shown in Addendum A will reduce the operating life of the BWO tube.

In addition, for instruments having Varian tubes, the anode supply voltage should never be adjusted so that this voltage, monitored as described in Paragraph 5.2 d, exceeds the maximum operating level shown in Addendum A or the tube data sheet. Anode current in Varian tubes (monitored as described in Paragraph 5.2 h) must not be allowed to exceed the trip-out level specified in Addendum A (or the maximum current rating specified in Addendum A for instruments without the anode protection circuit). Exceeding the voltage or current limits for the anode voids the warranty!

e. For units with internal sampling, set the RF OUTPUT control to LEVELED and the RF SAMPLER switch to INT. Then check the leveled power characteristic across the band, and also check leveler response to the POWER LEVEL control. The maximum power output obtainable with full-band leveling should occur at a mid-range setting of the POWER LEVEL control, and should equal or exceed the level specified in Table 1-1. Also check the leveled power variation across the band. The variation across the entire band should not exceed the amount specified in Table 1-1. If the unit does not meet leveled output specifications, check the INTERNAL SAMPLER SENSITIVITY and CONTROL VOLTAGE LIMIT adjustments as directed in Paragraph 5.3.2.

f. For units which require external sampling, set the RF OUTPUT control to LEVELED. Connect a suitable external rf sampler loop to drive the RF SAMPLER INPUT jack, as shown in Figure 5-3, by inserting a sampler or a coupler and detector between the RF OUTPUT jack and the attenuator pad. With a suitable sampler and a proper adjustment of the RF SAMPLER SENS. panel control, it should be possible to obtain full-band leveling with a mid-range setting of the POWER LEVEL control; and response to adjustment of the POWER LEVEL control above and below the full-band leveling point should be as described in the Model 650 Instruction Manual. The maximum power output obtainable with full-band leveling should approximately equal the level specified in Table 1-1. (An optimum external sampler loop using flat components can achieve the minimum variation specifications given in Table 1-1.) If leveler performance is unsatisfactory and the external sampler loop is known to be adequate, check the CONTROL VOLTAGE LIMIT adjustment as directed in Paragraph 5.3.2.

5.4 TROUBLE SHOOTING

NOTE: Refer to Paragraph 5.4 of the Model 650 Instruction Manual. The following material is supplementary to that manual.

5.4.1 BWO TUBE AGING AND FAILURE. There are three general indications of BWO tube aging: (1) reduced rf output throughout the operating band; (2) complete loss of rf output at one or more frequencies in the operating band; (3) repeated tripout of the helix overcurrent protection relay when operating unleveled with full output power, particularly in the upper portion of the operating band. Indications (1) and (2) above are usually caused by reduced cathode emission, while gas ionization in the BWO tube causes indication (3). Indication (2) at either band limit can be caused by a defective cathode supply or a defective or mis-adjusted diode shaper board that produces cathode voltage outside the proper tuning range. Normal aging of the BWO tube does not produce frequency calibration error; but internal arcing or other breakdown can produce excess residual fm.

To localize trouble to the BWO tube when indications of the above type are present, check all tube voltages and currents in accordance with Paragraph 5.2 above, and make adjustments as necessary following the procedures of Paragraph 5.3. If proper operation is not restored with proper voltages applied, and the BWO tube is still in warranty, read the Tube Warranty and Replacement Procedure at the rear of this manual and follow the specified return procedure. If the tube warranty

has expired, refer to the procedure of Paragraph 5.3.5 and try increasing the anode voltage above the normal level to restore power output, before discarding the tube.

NOTE: Where failure is due to gas ionization, no adjustment of voltages will restore normal, full-band operation. If desired, the tube can still be used within a restricted tuning range, at frequencies below the point where excessive helix current occurs.

5.4.2 ELEVATED SUPPLY FAILURE. When failure is suspected in the Elevated Supply PC Board A12, all the passive components can be checked with power turned off. If the passive components are satisfactory, and the correct grid voltage is not produced with suitable drive from the modulator, check the performance of A12Q1 by substitution.

5.4.3 DIODE SHAPER BOARD FAILURE. If the proper voltages cannot be obtained when adjusting the START FREQ and END FREQ adjustment controls, and voltage measurements on the cathode supply feedback amplifier indicate that it is operating properly, check the performance of the diode shaper board. Typical operating voltages on the resistor biasing string are indicated in Figure 4-3. Performance of the diodes can be checked with power off by using an ohmmeter to measure forward and reverse resistance. To avoid overloads, do not use any range where open-circuit voltage exceeds 1.5 V, or short-circuit current exceeds 3 ma. In-circuit voltage checks may also be used to check A13Q1, A13Q2, A13CR13 and A13CR14.

5.4.4 INTERNAL SAMPLER FAILURE. If the instrument operates properly with an external sampler known to be in good condition, but performance with internal sampling is unsatisfactory, check the adjustment of the INTERNAL SAMPLER SENSITIVITY control following the procedure of paragraph 5.3.2. Also check the crystal detector on the internal sampler by substitution. If the internal sampler or its crystal detector are replaced, reset the INTERNAL SAMPLER SENSITIVITY adjustment following the procedure of Paragraph 5.3.2.

5.4.5 LEVELER-AMPLIFIER IMBALANCE or FAILURE. When the CONTROL VOLTAGE LIMIT adjustment is not set properly, it can interfere with the normal smooth leveler response by unbalancing the feedback loop. When response does not seem consistent across the full swept band or at low and high power levels, re-adjust the CONTROL VOLTAGE LIMIT as directed in paragraph 5.3.2. If the CONTROL VOLTAGE LIMIT adjustment cannot restore proper operation, make voltage measurements in the leveler-amplifier under various operating conditions, in accordance with the table shown on schematic D900933. These measurements should localize the trouble to the defective component or stage - or to the PIN diode attenuator module itself.

5.4.6 PIN ATTENUATOR MODULE FAILURE. If the output is not leveled, and does not change in response to adjustment of the POWER LEVEL control, and if voltage checks on the leveler-amplifier (paragraph 5.4.5) indicate that a proper drive is being supplied at terminal A11-15, the PIN attenuator may be defective. To check the PIN attenuator, perform the following procedure:

a. First check that proper -12 V power is being supplied to the leveler from the PIN attenuator power supply A14. Measure this voltage between pin 1 of the PIN attenuator module (-) and the module body (+, ground) with a digital voltmeter. This can be done most easily by removing the plug-in unit from the Model 650 and applying 115 Vac across terminal 4 and 5 of A14 to drive the supply.

CAUTION

Double-check that the correct terminals are being used for this application of voltage. Otherwise costly damage to the instrument may occur.

b. If the voltage monitored in step a is not within the specification range of -12 ± 0.1 V, adjust it using A14R6.

c. Monitor the supply ripple by connecting an oscilloscope to the same terminals [pin 1 of the leveler module (-) and the module body (+, ground)]. Ripple should be < 5 mv.

d. If the voltage cannot be properly adjusted or has excessive ripple, check the power supply components for failure and replace as necessary. Then check and adjust the voltage in accordance with steps a and b above.

e. To check the PIN attenuator disconnect the temporary 115 Vac leads from terminals 4 and 5 of A14, and replace the Plug-in unit into the Model 650 main frame. The 0.5 V to -2.0 V control range provided at A11-15 in the unleveled mode (as verified by the measurements of paragraph 5.4.5) will be used to provide full-range drive to the leveler module for this test.

f. Make control settings as follows:

RF OUTPUT to UNLEVELED
FREQUENCY Mode Selector to SINGLE
SINGLE Mode Selector to F_0
F_0 control to band center
RETRACE to OFF
MARKER AMPLITUDE to OFF

g. Connect the RF OUTPUT connector to an rf power measurement loop such as shown in Figure 5-4.

h. Monitor the power output range provided when the POWER LEVEL control is adjusted over its full range from the CCW limit to the CW limit. The difference between minimum and maximum output should be as follows:

FREQ. RANGE OF INSTRUMENT (GHz)	PIN ATTENUATOR PART NO.	MINIMUM DIFFERENCE BETWEEN MIN. & MAX. OUTPUT (Unleveled Mode)
1 - 2	951041-02	25.5 db
≦1.7 - 4.2	951041-03	30.5 db
≦3.5 - 8.3	951041-04	26.5 db
≦ 7 - 12.4	951041-05	22 db

i. Adjust the F_0 control to each band limit and repeat the power control range test of step h. If adequate control range is provided across the band, the PIN attenuator module is probably operating properly. If no or insufficient control range is provided, the PIN attenuator module is probably defective and must be returned to the factory for repair.

CAUTION

The PIN diode attenuator module is not field repairable or field adjustable. Do not attempt to adjust or disassemble the attenuator or driver circuits in the field.

j. A final check of PIN attenuator performance can be made by removing the PIN attenuator module from the plug-in unit. Label the wires attached to pins 1, 7, 8, and the body, and then unsolder them. Then remove the leveler attaching hardware and disconnect the rf lines.

k. To supply -12 V power to the PIN attenuator for external testing, connect jumpers as follows:

Pin 1 of the PIN attenuator power supply to the attenuator module body.
Pin 6 of the PIN attenuator power supply to pin 1 of the attenuator module.
Pins 4 and 5 of the PIN attenuator power supply to 115 Vac.

l. Obtain a controlled, well regulated and filtered dc test source with an output in the range 0.5 - 2.0 Vdc.

m. Adjust the dc test source voltage to 0.5 V, and connect its + lead to the PIN attenuator module body (ground) and its - lead to pin 7 of the PIN attenuator module.

n. Connect the PIN diode attenuator into an rf power measurement loop such as shown in Figure 5-4, between the signal source attenuator and the detector. The source output and attenuator value should be selected to avoid applying more than 100 mw to the PIN attenuator.

o. Set the dc test source voltage (with polarity as specified in step m) first to 0.5 V and then to 2.0 V, and monitor the output level in each case. The leveler should provide minimum attenuation at a control voltage of 0.5 V, and maximum attenuation at a control voltage of 2.0 V. Attenuation provided in each case should be:

FREQ. RANGE OF PLUG-IN UNIT (GHz)	PIN ATTENUATOR PART No.	MIN. ATTENUATION	MAX. ATTENUATION
1 - 2	951041-02	≦2.5 db	≧28.0 db
≦1.7 - 4.2	951041-03	≦2.5 db	≧33.0 db
≦3.5 - 8.3	951041-04	≦3.5 db	≧30.0 db
≦ 7 - 12.4	951041-05	≦5.0 db	≧27.0 db

If attenuation is outside these limits, the attenuator must be returned to the factory for repair.

p. To measure PIN attenuator input matching, terminate the output connector in a 50 Ω load and connect a directional coupler to the input connector, oriented to pick up reflections from the PIN attenuator. Connect the detector to the coupler arm to measure the return loss using reflectometry techniques. Return loss with leveler control voltage anywhere in the 0.5 - 2.0 V range should be more than the following minimum:

FREQ. RANGE OF PLUG-IN UNIT (GHz)	PIN ATTENUATOR PART No.	MIN. RETURN LOSS (EITHER END)
1 - 2	951041-02	8.0 db
≦1.7 - 4.2	951041-03	9.0 db
≦3.5 - 8.3	951041-04	7.5 db
≦ 7 - 12.4	951041-05	6.5 db

q. Interchange the rf test connections to the PIN attenuator input and output connectors and repeat the return loss measurement of step p above. If return loss is less than the minimum specified at either end, the PIN attenuator must be returned to the factory for repair.

5.4.7 FAILURE OR MISADJUSTMENT OF ANODE PROTECTION CIRCUIT. (Units with Varian tubes only). The anode protection circuit is inherently reliable and is factory-adjusted to trip out when the maximum allowable anode current is reached for the particular tube type used in the Plug-in unit. This factory adjustment should rarely, if ever, require re-setting, since the circuit components are stable with time, and all replacement tubes of the same type require the same setting. If repeated trip-outs are traced to the anode protection circuit, the circuit can be checked and calibrated using the following procedure:

a. With the Model 650 Oscillator Plug-in unit disconnected from the Model 650 main frame, connect a variable power supply between terminal #1 (+) and #2 (-) of the 951619 Anode Protection Circuit (see Figure 5-1 for location). The power supply must be current limited or protected, since the circuit will draw 250 ma when tripped.

b. Connect a variable 10K resistor (set to maximum resistance) and a D.C. milliameter in series, between terminal #2 and #3 of the Anode Protection Circuit.

c. Set Anode Protection calibration potentiometer A19R2 fully CW, and apply 50 volts between terminal #1 and #2 with the variable power supply.

d. Adjust the variable 10K resistor until the milliammeter reads one milliamp less than the maximum anode current shown for the specific Varian BWO tube type, in Table 5-1. (e.g., BWO Tube Type VA160A would require an adjustment to obtain anode current of 10 ma.)

e. Now calibrate the Anode Protection Circuit by rotating calibration potentiometer A19R2 CCW until trip occurs. A trip condition is indicated when current out of the power supply rises sharply. The Anode Protection Circuit is now calibrated for use in all modified 650 main frames. If the Anode Protection Circuit cannot be properly calibrated to the specified trip-out current level, it is defective and a replacement Anode Protection PC board should be obtained from the factory.

f. Disconnect the variable power supply, the 10K variable resistor, and the milliameter from the Anode Protection Circuit.

TABLE 5-1. MAXIMUM ANODE CURRENT FOR VARIAN TUBES.

TYPE	FREQUENCY RANGE	ANODE TRIP CURRENT
VA-183AA	.99 to 2.1 GHz	11 ma
VA-183A	1 to 2 GHz	"
VA-186AA	1.4 to 2.5 GHz (Tentative)	"
VA-184AA	1.7 to 4.2 GHz	"
VA-184A, VA184A Spec.	2 to 4 GHz	"
VA-187AA	2.5 to 5.5 GHz (Tentative)	"
VA-187A	2.6 to 5.2 GHz (Tentative)	"
VA-185AA	3.5 to 6.75	"
VA-185AC	3.6 to to 4.3, 5.9 to 6.5 GHz	"
VA-185AB	3.7 to 8.3 GHz	"
VA-185A	4 to 8 GHz	"
VA-160A, VA-160W	4 to 8 GHz	"
VA-160A, Special Spec. #2	5.3 to 6.5 GHz, Spec. #2, 5.9 to 6.5	"
VA-160C	5.4 to 5.9 GHz	13 ma
VA-173A, VA-173A Spec. #1	7 to 12.4 GHz	11 ma
VA-173E, VA-173W	8 to 12.4 GHz	"
VA-175A	8 to 12.4 GHz	"
VA-177A	10 to 15.5 GHz	7 ma
VA-162A Spec. #3	10 to 18 GHz	"
VA-162A Special	10 to 20 GHz	"
VA-162A Spec. #2	10 to 20 GHz Hi Power	"
VA-162A, VA-162W	12.4 to 18 GHz	"
VA-163A, VA-163A Spec. #1	18 to 26.5 GHz, VA-163A Spec. #1 17 to 26.5 GHz	"
VA-163A Spec. #2	20 to 28 GHz	"
VA-164A, 164A Spec. #1	26.5 to 40 GHz	"

5.5 PARTS REPLACEMENT

5.5.1 REPLACING THE BWO TUBE. Refer to Paragraphs 5.7.1 and 5.7.2 of the Model 650 Instruction Manual. After the tube is replaced, replace the time meter and adjust operating voltages as directed in Paragraph 5.7.5 of the Model 650 Instruction Manual. Then perform final frequency calibration in accordance with Paragraph 5.3.3 of this manual.

NOTE: Always retain the data sheet supplied with the new tube for future reference, by placing it in the envelope at the rear of this manual.

5.5.2 REPLACING INDIVIDUAL CIRCUIT COMPONENTS. Refer to Figures 5-1 and 5-2 of this manual for the locations of circuit board and major rf components. Parts on all PC boards are identified by silkscreened reference designators. These parts are also shown on the overall schematics in Section 7. The proper soldering techniques for parts replacement are given in Paragraph 5.8 of the Model 650 Instruction Manual.

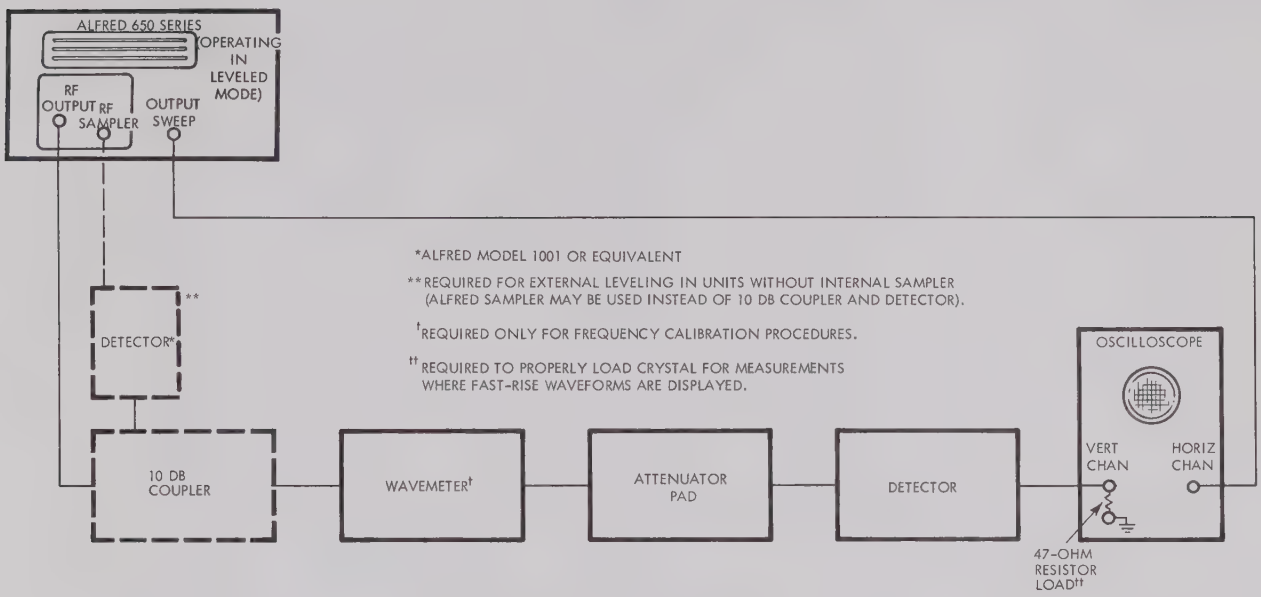

ALFRED 650 SERIES
(OPERATING IN LEVELED MODE)
RF OUTPUT
RF SAMPLER
OUTPUT SWEEP
*ALFRED MODEL 1001 OR EQUIVALENT
** REQUIRED FOR EXTERNAL LEVELING IN UNITS WITHOUT INTERNAL SAMPLER (ALFRED SAMPLER MAY BE USED INSTEAD OF 10 DB COUPLER AND DETECTOR).
† REQUIRED ONLY FOR FREQUENCY CALIBRATION PROCEDURES.
†† REQUIRED TO PROPERLY LOAD CRYSTAL FOR MEASUREMENTS WHERE FAST-RISE WAVEFORMS ARE DISPLAYED.
DETECTOR*
**
10 DB COUPLER
WAVEMETER†
ATTENUATOR PAD
DETECTOR
OSCILLOSCOPE
VERT CHAN
HORIZ CHAN
47-OHM RESISTOR LOAD††

FIGURE 5-3. BASIC RF TEST SET-UP

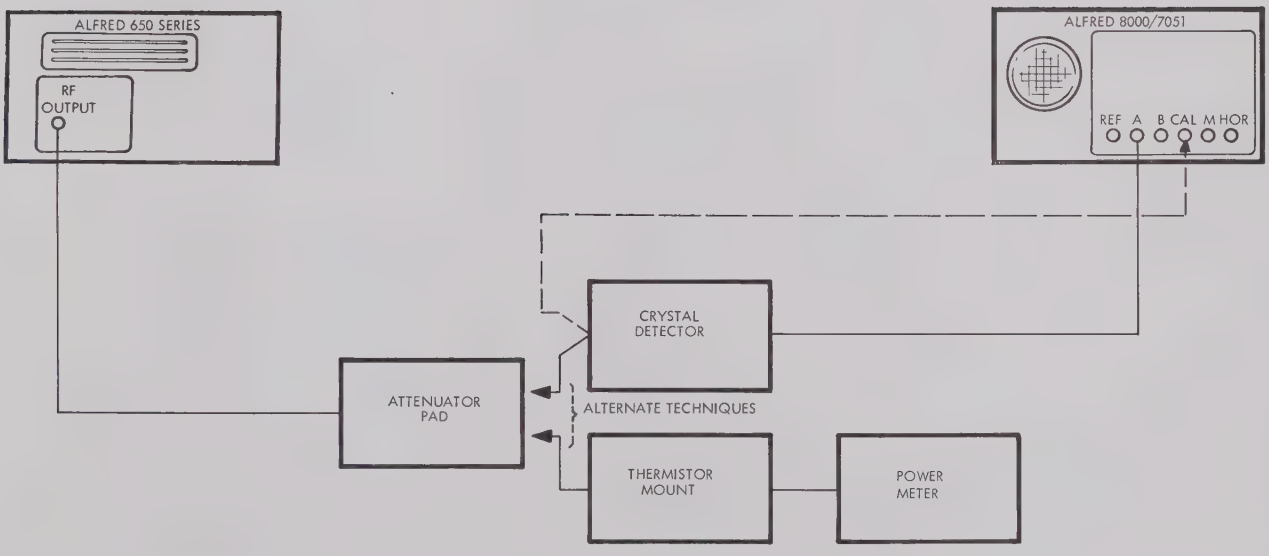


FIGURE 5-4. POWER CALIBRATION TEST SET-UP

PIN DIODE PLUG-IN CHASSIS ASSEMBLY

CIRCUIT REFERENCE	ALFRED PART NO.	DESCRIPTION
DS1	126-11	Lamp, clear, 110 v
J1	133-62	Socket, P.C. Plug-in 10 pin min. #K2105
J2	133-63	Socket, P.C. Plug-in 22 pin min. #K2225
J3 and J4	131-UG1094	Connector, BNC-Panel, UG 1094 U
J5	133-77	Jack; Telephone
M1	87-3	Elapsed time meter
R1	51-330Ω	Resistor, fixed, dep. carbon, 330Ω, 1/2 w, ±5%
R2	51-390Ω	Resistor, fixed, dep. carbon, 390Ω, 1/2w, ±5%
R3	51-15K	Resistor, fixed, dep. carbon, 15K, 1/2w, ± 5%
R4	51-56K	Resistor, fixed, dep. carbon, 56K, 1/2w, ± 5%
R5	56-69.8K	Resistor, fixed, MF, 69.8K, 1/2 w, ±1%
R6	62-1	Resistor, fixed WW, 10K, 10W, ± 5%
R7	71-133	Resistor, variable, 1.5K, .3w, ± 30%
R8	51-1.5K	Resistor, fixed, dep. carbon, 1.5K, 1/2w, ±5%
R9	(102-134)	Resistor, variable, 10K (part of S2)
S1	102-136	Switch, slide, DPDT
S2	102-134	Switch, rotary with 1K pot (R9)
TP21	133-29	Tip jack, black
TP22 and TP23	133-28	Tip jack, red
XM1	122-2	Time meter holder (fuse holder)

A11 PIN DIODE LEVELER AMPLIFIER PC BOARD (ASSY. 951088)

CIRCUIT REFERENCE	ALFRED PART NO.	DESCRIPTION
A11C1 and A11C2	14-37	Capacitor, fixed, film, 0.1 µf, ± 10%, 80 v
A11C3	14-42	Capacitor, fixed, porcelain, 0.0082 µf, ± 10%, 80 v, Sprague 192 P
A11C4	11-36	Capacitor, fixed, dipped mica, 150 pf, ± 10%, 100 v
A11C5	11-25	Capacitor, fixed, dipped mica, 47 pf, ± 5%
A11C6	11-37	Capacitor, fixed, dipped mica, 200 pf, ± 10%, 100 v
A11C7 and A11C8	11-28	Capacitor, fixed, dipped mica, 82 pf, ± 2%
A11C9		Same as A11C1
A11C10		Capacitor, fixed, 3.3 µf, 100 v
A11C11		Capacitor, fixed, 0.001 µf, 100 v
A11CR1 and A11CR2	81-132	Diode, 1N914
A11Q1	81-194	Transistor, Sprague TD100
A11Q2	81-115	Transistor, 2N3565
A11Q3 and A11Q4	81-140	Transistor, 2N5087
A11Q5		Same as A11Q2
A11Q6		Same as A11Q1
A11Q7 thru A11Q11	81-189	Transistor, 2N4917
A11Q12 and A11Q13	81-169	Transistor, 2N3569
A11Q14	81-72	Transistor, NPN, 2N699
A11R1	59-26.7 K	Resistor, fixed, MF, 26.7 K, 1/4 w, ± 1%
A11R2	59-26.1 K	Resistor, fixed, MF, 26.1 K, 1/4 w, ± 1%
A11R3 and A11R4	59-1 K	Resistor, fixed, MF, 1 K, 1/4 w, ± 1%
A11R5	71-136	Resistor, variable, comp., 250Ω, 0.2 w, ± 30%

A11 PIN DIODE LEVELER AMPLIFIER PC BOARD (con'd)

CIRCUIT REFERENCE	ALFRED PART NO.	DESCRIPTION
A11R6 and A11R7	34-10 K	Resistor, fixed, comp., 10 K, 1/4 w, ± 5%
A11R8	34-4.7 K	Resistor, fixed, comp., 4.7 K, 1/4 w, ± 5%
A11R9		Same as A11R6
A11R10 and A11R11	34-560Ω	Resistor, fixed, com., 560Ω, 1/4 w, ± 5%
A11R12 and A11R13		Same as A11R6
A11R14	34-5.6 K	Resistor, fixed, comp., 5.6 K, 1/4 w, ± 5%
A11R15	34-4.3 K	Resistor, fixed, comp., 4.3 K, 1/4 w, ± 5%
A11R16	34-1.8 K	Resistor, fixed, comp., 1.8 K, 1/4 w, ± 5%
A11R17	71-135	Resistor, variable, comp., 1.5 K, 0.2 w, ± 30%
A11R18		Same as A11R14
A11R19 and A11R20	34-390Ω	Resistor, fixed, comp., 390Ω, 1/4 w, ± 5%
A11R21		Same as A11R8
A11R22	34-8.2 K	Resistor, fixed, comp., 8.2 K, 1/4 w, ± 5%
A11R23	34-1 K	Resistor, fixed, comp., 1 K, 1/4 w, ± 5%
A11R24		Same as A11R6
A11R25	34-15 K	Resistor, fixed, comp., 15 K, 1/4 w, ± 5%
A11R26	34-330Ω	Resistor, fixed, comp., 330Ω, 1/4 w, ± 5%
A11R27		Same as A11R26
A11R28 and A11R29	34-820Ω	Resistor, fixed, comp., 820Ω, 1/4 w, ± 5%
R11R30		Same as A11R8
A11R31		Same as A11R6
A11R32	34-2.2 K	Resistor, fixed, comp., 2.2 K, 1/4 w, ± 5%
A11R33	34-220Ω	Resistor, fixed, comp., 220Ω, 1/4 w, ± 5%
A11R34		Same as A11R26
A11R35		Same as A11R23
A11R36		Same as A11R28
A11R37 and A11R38		Same as A11R23
A11R39	34-1.5 K	Resistor, fixed, comp., 1.5 K, 1/4 w, ± 5%
A11R40 and A11R41		Same as A11R25
A11R42	71-134	Resistor, variable, comp., 500Ω, 2 w, ± 30%
A11R43	34-33 K	Resistor, fixed, comp., 33 K, 1/4 w, ± 5%
A11R44	34-12	Resistor, fixed, comp., 12 K, 1/4 w, ± 5%
A11R45 and A11R46		Same as A11R23
A11R47	59-100 K	Resistor, fixed, MF, 100 K, 1/4 w, ± 1%
A11R48	59-133 K	Resistor, fixed, MF, 133 K, 1/4 w, ± 1%

A14 -12 V SUPPLY PC BOARD (ASSY. 951087)

CIRCUIT REFERENCE	ALFRED PART NO.	DESCRIPTION
A14C1	13-72	Capacitor, fixed, elect., 1000 mf, 25v
A14C2	12-52	Capacitor, fixed, ceramic, 0.47 µf, 25 v
A14C3	13-49	Capacitor, fixed, elect., 50 µf, 25 v
A14CR1	81-178	Rectifier Bridge Assy., Motorola MDA-920A-2
A14CR2	81-123	Didoe, Zener, 1N749A
A14Q1	81-133	Transistor, RCA40372
A14Q2	81-114	Transistor, 2N3638A
A14Q3	81-169	Transistor, 2N3569
A14R1	51-10K	Resistor, fixed, dep. carbon, 10K, 1/2w, 5%
A14R2	51-1.5K	Resistor, fixed, dep., carbon, 1.5K, 1/2w, 5%
A14R3	51-1.2K	Resistor, fixed, dep. carbon, 1.2K, 1/2w, 5%
A14R4	51-390Ω	Resistor, fixed, dep., carbon, 390Ω, 1/2w, 5%
A14R5	51-680Ω	Resistor, fixed, dep., carbon, 680Ω, 1/2 w, 5%
A14R6	71-95	Resistor, variable, 2 K, 1w, 10%, 20 turn
A14T1	91-205	Transformer

* Factory Selected

ELEVATED SUPPLY PC BOARD (ASSEMBLY A12)
PART NO. 950620 (Early Production)
PART NO. 901488 (Later Production)

CIRCUIT REFERENCE	ALFRED PART NO.	DESCRIPTION
A12C1	12-28	Capacitor, fixed, ceramic, 0.001 mf, 6 kv
A12C2	12-1	Capacitor, fixed, ceramic, 15 pf, 600 v (in Assy. 950620)
	11-2	Capacitor, fixed, ceramic, 24 pf, 600 v (in Assy. 901488)
A12C3 and A12C4	12-3	Capacitor, fixed, ceramic, 0.01 µf., 500 v
A12CR1 and A12CR2	81-41	Diode, 1N459
A12CR3 and A12CR4	81-41	Diode, 1N459 (in Assy. 901488)
A12CR5 and A12CR6	81-156	Diode, Zener, 15 v, 1 w, MZ1000-17/1N4744 (in Assy. 901488)
A12Q1	81-72	Transistor, 2N699
A12R1	51-150K	Resistor, fixed, comp., 150 K, 1/2 w, ±5%
A12R2		Factory installed (see Addendum A)
A12R3	46-1 K	Resistor, fixed, MF, 1 K, 2 w, 5%
	46-820Ω	Alt. value 820 Ω (in Assy. 950620) Not used (in Assy. 901488)
A12R4	46-750 Ω	Resistor, fixed, MF, 750 Ω, 2 w, 5% Alt. value 820 Ω (in Assy. 950620)
	35-1.1K	Resistor, fixed, MF, 1.1 K, 2 w, 5% (in Assy. 901488)
A12R5	46-750 Ω	Resistor, fixed, MF, 750 Ω, 2 w, 5% (in Assy. 950620)
	50-820Ω	Resistor, fixed, MF, 820 Ω, 2 w, 5% (in Assy. 901488)
A12R6	41-1.8 K	Resistor, fixed, comp., 1.8 K, 1/2 w, 10% (in Assy. 950620)
	51-7.5 K	Resistor, fixed, comp., 7.5 K, 1/2 w, 5% (in Assy. 901488)
A12R7*	42-18 K	Resistor, fixed, comp., 18 K, 1w, 10% (in Assy. 950620)
	45-11K	Resistor, Factory Select, Nom. Value 11K, 1 w, 5% (in Assy. 901488)
A12R8	51-10Ω	Resistor, fixed, comp., 10 Ω, 1/2 w, ±5%
A12R9	71-103	Resistor, variable, 50 K, (cts type 201)
A12R10	56-237 K	Resistor, fixed, MF, 237 K, 1/2 w, 1%
A12R11	51-8.2K	Resistor, fixed, comp., 8.2 K, 1/2 w, ±5%
A12R12		Not used except as a substitute for A12R14 (see Addendum A)
A12R13	56-10 Ω	Resistor, fixed, MF, 10 Ω, 1/2 w, 1%
A12R14		Factory selected (see Addendum A)
A12R15		Factory-selected (see Addendum A) (in Assy. 901488)
A12R16		Not used
A12R17	51-180 K	Resistor, fixed, carbon film, 180 K, 1/2 w, 5%
A12R18	51-27 K	Resistor, fixed, carbon film, 27 K, 1/2 w, 5%
A12L1	92-22	Coil, RF, 33 µh
A12TP1 thru A12TP3	133-85	Jack, Test Point

DIODE SHAPER PC BOARD (ASSEMBLY A13; See Table 1-1 for Part No.'s)

CIRCUIT REFERENCE	ALFRED PART NO.	DESCRIPTION
A13C1 thru A13C3	12-34	Capacitor, fixed, ceramic, 0.1 µf, 100 v
A13C4	12-22	Capacitor, fixed, ceramic, 100 pf, 500 v
A13C5 and A13C6		Same as A13C1
A13CR1 thru A13CR14	81-41	Diode, 1N459
A13Q1 and A13Q2	81-115	Transistor, 2N3565
A13R1 thru A13R11		See Table 7-1 on schematic 900713C
A13R12 thru A13R14		Not used
A13R15	56-750Ω	Resistor, fixed, MF, 750Ω, 1/2 w, 1%
A13R16	56-121Ω	Resistor, fixed, MF, 121Ω, 1/2 w, 1%
A13R17 thru A13R23	56-100Ω	Resistor, fixed, MF, 100 Ω, 1/2 w, 1%
A13R24	56-80.6Ω	Resistor, fixed, MF, 80.6Ω, 1/2 w, 1%
A13R25	56-1K	Resistor, fixed, MF, 1K, 1/2 w,1%
A13R26	71-90	Resistor, variable, WW, 200 Ω, 20 turns
A13R27	41-1.8 K	Resistor, fixed, comp., 1.8 K, 1/8 w, 10%
A13R28	71-94	Resistor, variable, WW, 5K, 20 turns
A13R29	41-27K	Resistor, fixed, comp., 27K, 1/2 w, 10%
A13R30	41-10K	Resistor, fixed, comp., 10K, 1/2w, 10%
A13R31	41-150 K	Resistor, fixed, comp., 150 K, 1/ w, 10%

BWO TUBE (See Table 1-1)

PIN ATTENUATOR MODULE (See listing of assy. no. vs. frequency range of plug-in unit on page 5-9)

OUTPUT LOW-PASS FILTER AND INTERNAL SAMPLER (As applicable; see Table 1-1)

A19 ANODE PROTECTION PC BOARD (Assy. 951619)[tt]

CIRCUIT REFERENCE	ALFRED PART NO.	DESCRIPTION
A19CR1	81-190	Diode, IN270
A19CR2 and A19CR3	81-20	Diode, IN4385
A19Q1	81-181	Transistor, 2N4889
A19Q2	81-114	Transistor, 2N3638A
A19Q3	81-182	Rectifier, Silicon controlled C106C1
A19Q4		Same as A19Q2
A19R1	51-10K	Resistor, fixed, comp., 10K, 1/2 w, ± 5%
A19R2	71-99	Resistor, variable, comp., 100 Ω, 2w
A19R3	51-22K	Resistor, fixed, comp., 22K, 1/2w, ± 5%
A19R4 and A19R5	56-4.64K	Resistor, fixed, M.F., 4.64K, 1/2w, ± 5%
A19R6	56-31.6K	Resistor, fixed, M.F., 31.6K, 1/2w, ± 5%
A19R7	43-270Ω	Resistor, fixed, comp., 270Ω, 2w, ± 10%
A19R8	41-4.7K	Resistor, fixed, comp., 4.7K, 1/2w, ± 10%
A19R9	41-100Ω	Resistor, fixed, comp., 100Ω, 1/2w, ± 10%

[tt] used only on units with Varian tubes

Factory-installed in later production units; field-installed in other units.

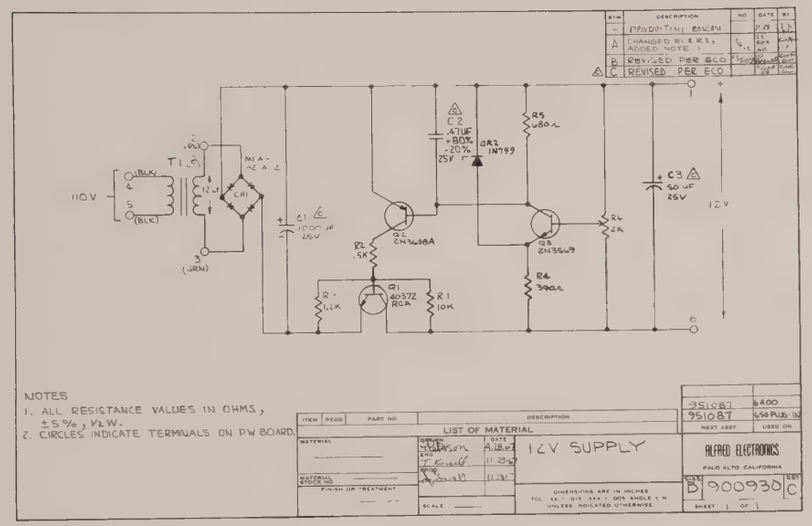
* Factory Selected

SECTION 7

SCHEMATIC DIAGRAMS

Schematics are included in the following order:

OVERALL CHASSIS SCHEMATIC	(D907053)	
PIN DIODE ATTENUATOR POWER SUPPLY A14	(B900930)	(on apron of D907053)
ANODE PROTECTION PC BOARD A19	(B907304)	(on apron of D907053)
LEVELER-AMPLIFIER PC BOARD A11	(D900933)	
ELEVATED SUPPLY PC BOARD A12	(C900869)	
DIODE SHAPER PC BOARD A13	(C900713)	



NOTES
1. ALL RESISTANCE VALUES IN OHMS, ±5%, ½W.
2. CIRCLES INDICATE TERMINALS ON PW BOARD.

ITEM	REQD	PART NO	DESCRIPTION
		951087	6400
		951087	650 PLUS-III

LIST OF MATERIAL — NEXT ASSY — USED ON

DRAWN	Wilson	11-25-68
ENG	T. Kissel	11-25-68
PROJ		11-26-68

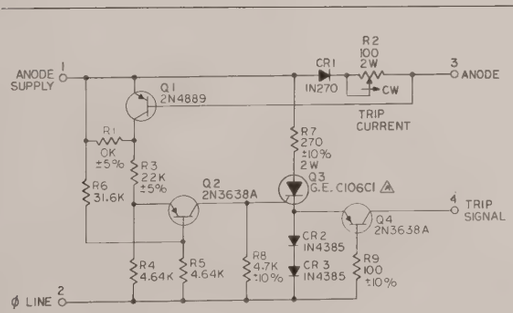
12V SUPPLY

DIMENSIONS ARE IN INCHES. TOL .XX ± .015, .XXX ± .005, ANGLE ± ½° UNLESS INDICATED OTHERWISE

ALFRED ELECTRONICS, PALO ALTO CALIFORNIA

SIZE B — 900930 — REV C — SHEET 1 OF 1

SYM	DESCRIPTION	NO	DATE	BY
–	PRODUCTION RELEASE		11-25-68	LK
A	CHANGED R1, R2, R3; ADDED NOTE 1	L-1	12-6-68	LK
B	REVISED PER ECO	1500	12-20-68	LK
C	REVISED PER ECO	1507	1-29-69	LK



NOTES
1. UNLESS OTHERWISE SPECIFIED ALL RESISTOR VALUES ARE IN OHMS, ±1%, 1/2 W.

SYM	DESCRIPTION	NO	DATE	BY
–	PROD RELEASE		3-6-68	
A	Q3 WAS 10366	1627	4-16-68	

ITEM	REQD	PART NO.	DESCRIPTION
		951619	650 MD

LIST OF MATERIAL — NEXT ASSY — USED ON

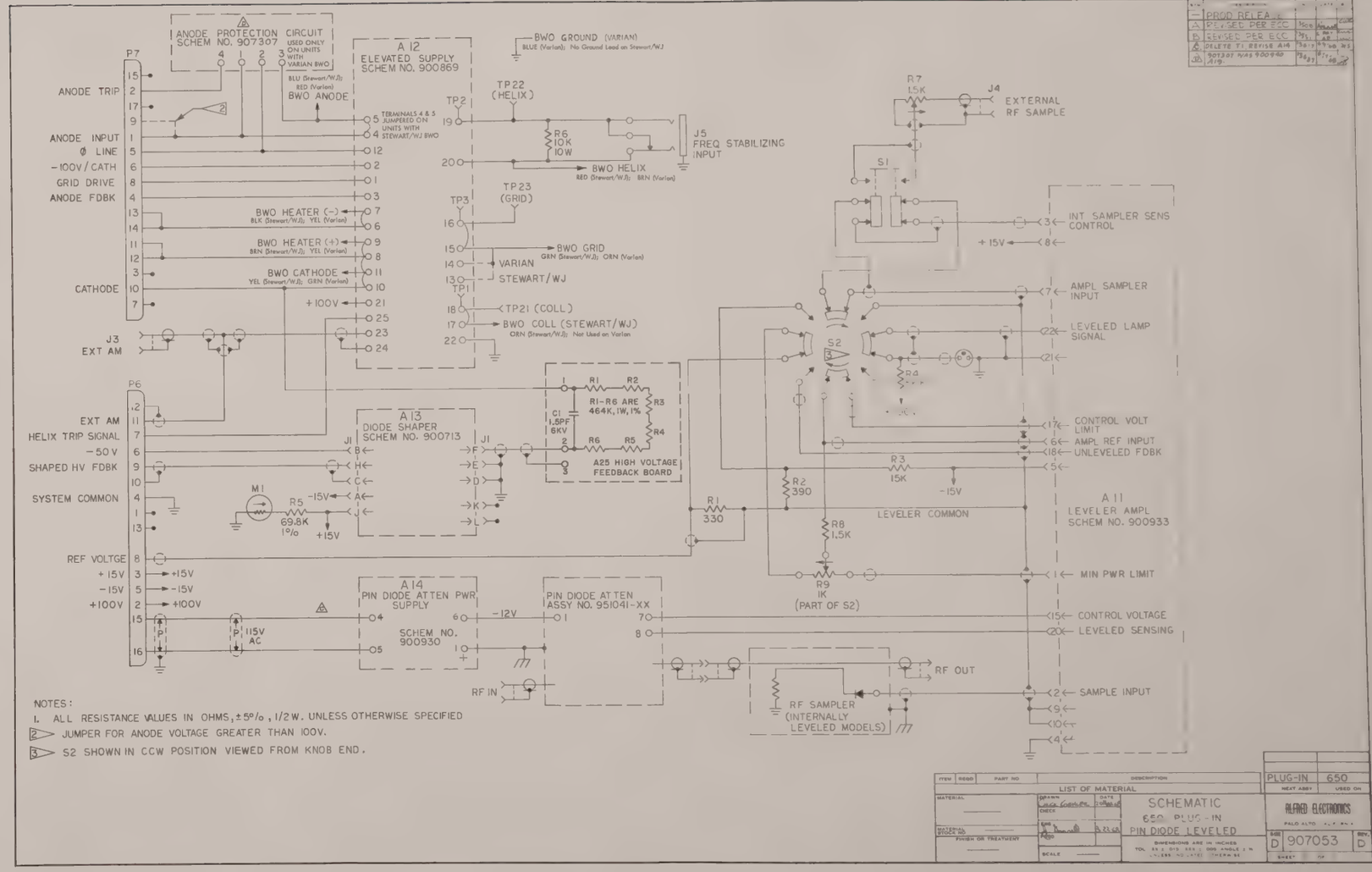
DRAWN	Lee Cowles	1-29-68
CHECK	R. Wilson	1-29-68
ENG		
PROJ		2/21/68

SCHEMATIC ANODE PROTECTION

DIMENSIONS ARE IN INCHES. TOL .XX ± .015, .XXX ± .005, ANGLE ± ½° UNLESS INDICATED OTHERWISE

ALFRED ELECTRONICS, PALO ALTO CALIFORNIA

SIZE B — 907307 — REV A — SHEET 1 OF 1



NOTES:
1. ALL RESISTANCE VALUES IN OHMS, ±5%, 1/2 W. UNLESS OTHERWISE SPECIFIED
2. JUMPER FOR ANODE VOLTAGE GREATER THAN 100V.
3. S2 SHOWN IN CCW POSITION VIEWED FROM KNOB END.

SYM	DESCRIPTION	NO	DATE	BY
–	PROD RELEASE		3-6-68	
A	REVISED PER ECO	1506	4-16-68	
B	REVISED PER ECO	1571	8-8-68	
C	DELETE T1, REVISE A14	1613	9-30-68	
D	907307 WAS 900940 A19	1683	11-20-68	

ITEM	REQD	PART NO	DESCRIPTION
		PLUG-IN	650

LIST OF MATERIAL — NEXT ASSY — USED ON

DRAWN	Lee Cowles	3-23-68
CHECK		
ENG		
PROJ		

SCHEMATIC 650 PLUG-IN PIN DIODE LEVELED

DIMENSIONS ARE IN INCHES. TOL .XX ± .015, .XXX ± .005, ANGLE ± ½° UNLESS INDICATED OTHERWISE

ALFRED ELECTRONICS, PALO ALTO CALIFORNIA

SIZE D — 907053 — REV D — SHEET 1 OF

TABLE OF VOLTAGE MEASUREMENTS ON LEVELER-AMPLIFIER A11 PC BOARD

TEST POINT	LOCATION		MEASUREMENT CONDITION (Operating Mode)* & MEASURED VOLTAGES (All Measurements in Volts)			
			LEVELED		UNLEVELED	
			a*	b*	c*	d*
A	Q1A	Base	- .073	- .018	- .011	- .011
B	Q1B	Base	- .062	- .008	0	0
C	Q3	Base	+ 2.33	+ 2.35	+2.34	+2.34
D	Q4	Base	+ 2.40	+ 2.39	+2.39	+2.39
E	Q5	Base	- 8.57	- 8.66	-8.63	-8.72
F	Q7	Base	+ 3.92	+ 4.56	+4.32	+4.95
G	Q8B	Base	+ 0.16	+ 4.68	+4.76	+4.76
H	Q10 and Q11	Base	- 1.57	- 1.89	-1.09	-2.75
J	Q11	Emitter	- 0.91	- 1.23	-0.43	-2.07
K	CR2	Cathode	+ 0.56	+ 0.29	+1.00	-0.49
L	PIN 18		- 0.52	- 0.53	+1.00	-0.48
M	Q12	Base	- 3.72	- 4.39	-3.43	-3.79
N	Q12	Emitter	- 1.08	- 0.80	+0.08	+0.10
O	Q14	Base	- 0.46	- 0.19	+0.01	+0.52
P	Q14	Collector	+21.5	+21.6	0	+0.05

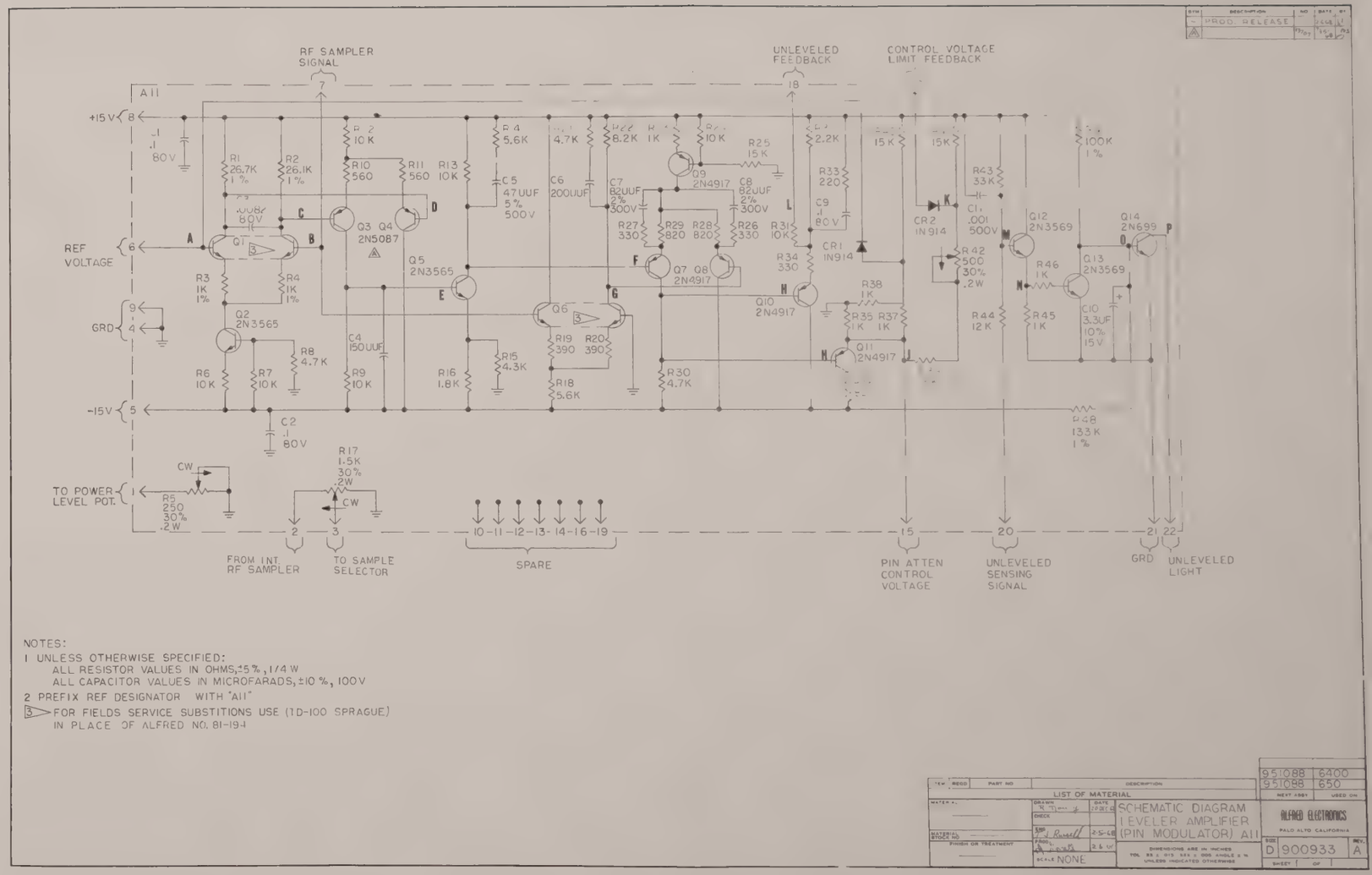
* DC VOLTAGES SHOWN ARE TYPICAL, MEASURED WITH RESPECT TO CHASSIS GROUND DURING SINGLE-FREQUENCY OPERATION, USING A HIGH-IMPEDANCE DIGITAL METER. AN ATTENUATOR OR OTHER SUITABLE LOAD SHOULD BE CONNECTED TO THE RF OUTPUT JACK.

a. Measured in LEVELED mode with POWER LEVEL control set at the mid-band point which provides maximum power with full-band leveling. (this point can be found by adjusting the POWER LEVEL control to the point where the UNLEVELED light is turned off during full-band sweep before switching to single-frequency operation for measurements.)

b. Measured in LEVELED mode with POWER LEVEL control fully CCW (minimum power).

c. Measured in UNLEVELED mode with POWER LEVEL control fully CW (maximum power).

d. Measured in UNLEVELED mode with POWER LEVEL control fully CCW (minimum power).


RF SAMPLER SIGNAL
UNLEVELED FEEDBACK
CONTROL VOLTAGE LIMIT FEEDBACK
A11
+15V
REF VOLTAGE
GRD
-15V
TO POWER LEVEL POT.
FROM INT. RF SAMPLER
TO SAMPLE SELECTOR
SPARE
PIN ATTEN CONTROL VOLTAGE
UNLEVELED SENSING SIGNAL
GRD
UNLEVELED LIGHT
Q1 Q2 Q3 Q4 Q5 Q6 Q7 Q8 Q9 Q10 Q11 Q12 Q13 Q14
CR1 CR2
R1 R2 R3 R4 R5 R6 R7 R8 R9 R10 R11 R13 R15 R16 R17 R18 R19 R20 R25 R27 R28 R29 R30 R31 R33 R34 R35 R37 R38 R42 R43 R44 R45 R46 R48
C1 C2 C4 C5 C6 C7 C8 C9 C10 C11

NOTES:
1 UNLESS OTHERWISE SPECIFIED:
ALL RESISTOR VALUES IN OHMS, ±5%, 1/4 W
ALL CAPACITOR VALUES IN MICROFARADS, ±10%, 100V
2 PREFIX REF DESIGNATOR WITH "A11"
3 FOR FIELDS SERVICE SUBSTITIONS USE (1D-100 SPRAGUE) IN PLACE OF ALFRED NO. 81-194

ITEM	REQD	PART NO	DESCRIPTION	95108	6400
			LIST OF MATERIAL	95108	650

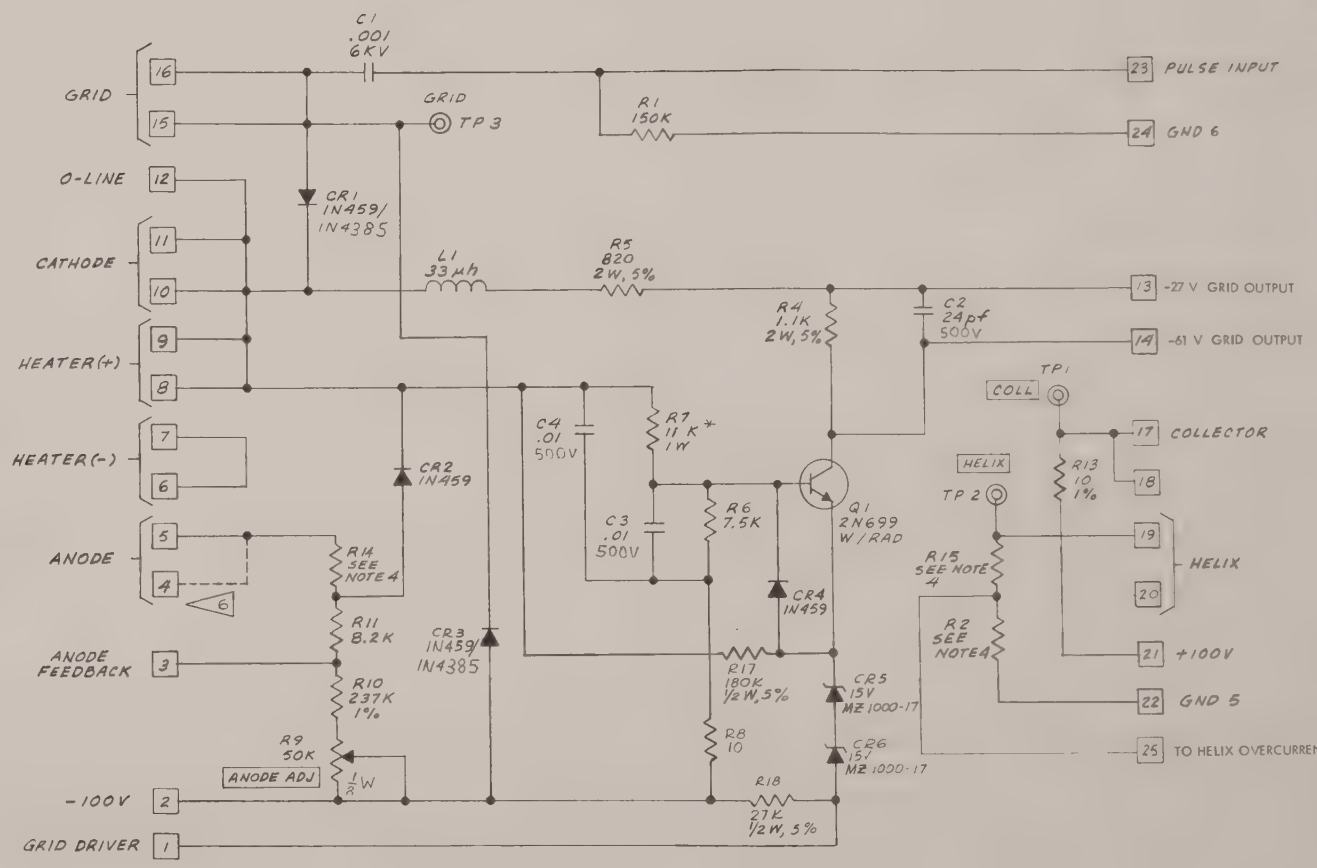
SCHEMATIC DIAGRAM
LEVELER AMPLIFIER
(PIN MODULATOR) A11

ALFRED ELECTRONICS
PALO ALTO, CALIFORNIA

SIZE D | 900933 | REV A

SCALE NONE

SYM	DESCRIPTION	NO	DATE	BY
A	CKT REVISED REFER TO E.C.O.	13164	3 8 67	MS
B	CKT REVISED REFER TO E.C.O.	13175	5 24 67	MS
C	ADDED NOTE 6	13245	8	



REFERENCE DESIGNATIONS		
FIRST	LAST	NOT USED
C1	C4	
CR1	CR6	
L1	L1	
Q1	Q1	
R1	R18	R3,R12,R16

NOTES:

1. UNLESS OTHERWISE SPECIFIED:
   1a. ALL RESISTORS IN OHMS, $\frac{1}{2}$W, 10%
   1b. ALL RESISTORS VALUED 1% ARE METAL FILM.
   1c. ALL CAPACITORS IN MICROFARADS, 500V
2. SQUARES (□) DESIGNATE TERMINAL CONNECTIONS ON ASSY "A12" ASSEMBLY NO. 901488.
3. PREFIX REFERENCE DESIGNATORS BY "A12" (I.E.: A12R1, A12Q1)
4. REFER TO INSTRUCTION MANUAL.
5. ASSY P.C. BD. NUMBER IS 944126C

◁6 ON INSTRUMENTS USING THE ANODE PROTECTION CIRCUIT 951155, THE CONNECTION BETWEEN A12-4 & A12-5 IS BROKEN.

ITEM	REQD	PART NO	DESCRIPTION

LIST OF MATERIAL

901488	KS 19974-L1	
901485	650 PLUG-IN	
901488	6400	
NEXT ASSY	USED ON	

MATERIAL

MATERIAL STOCK NO

FINISH OR TREATMENT

DRAWN M. SHEPPARD DATE 11-22-66

ENG

PROD

SCALE

SCHEMATIC ELEVATED SUPPLY

DIMENSIONS ARE IN INCHES
TOL .XX ± .015 .XXX ± .005 ANGLE ± ½°
UNLESS INDICATED OTHERWISE

ALFRED ELECTRONICS
PALO ALTO CALIFORNIA

SIZE C | 900869 | REV E

SHEET 1 OF 1

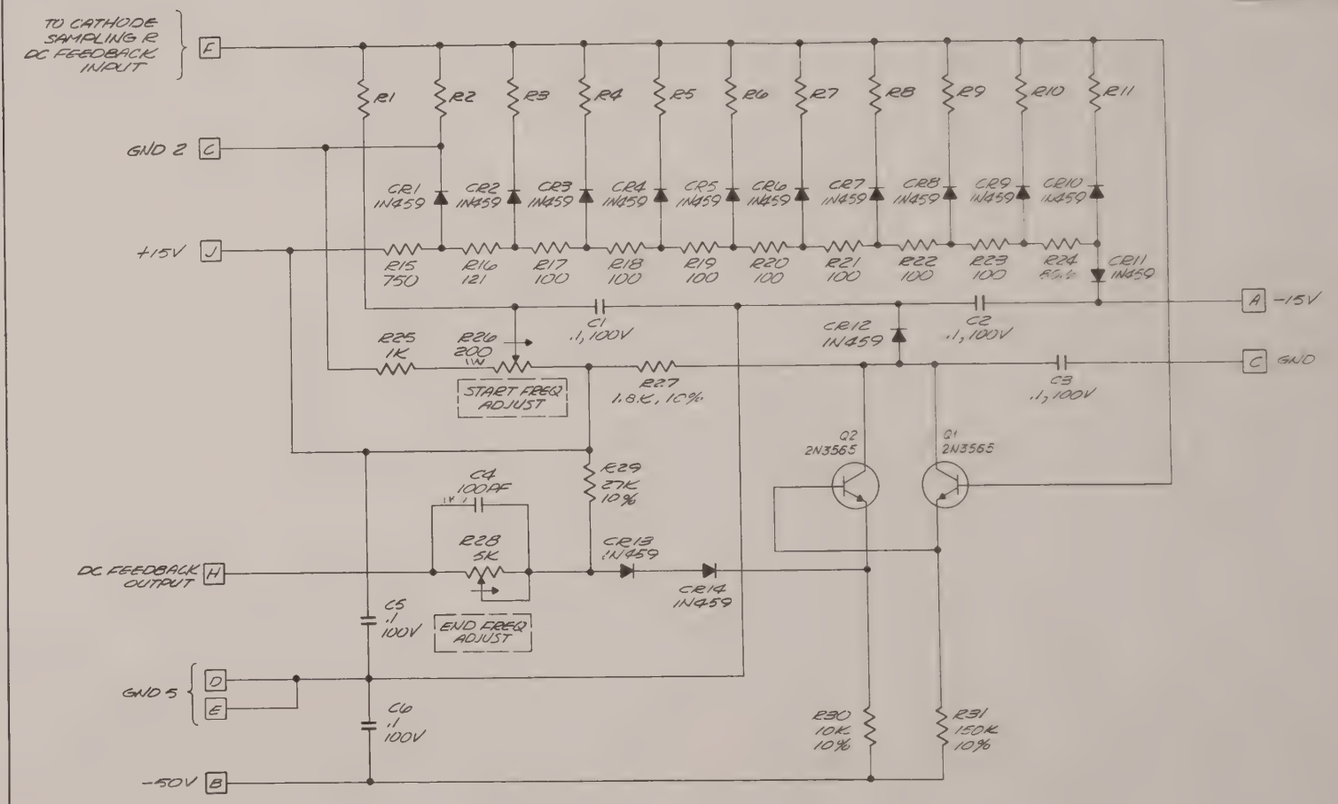
Table of Resistor Values for the Diode Shaper Assembly

Frequency GHz.	6400 Model No.	650 Model No.†	** Tube Manufacturer	Tube No.	R1	R2	R3	R4	R5	R6	R7	R8	R9	R10	R11
1 to 2	6410 thru 6415	651 651C* 651K 651CK*	Stewart	SE 214C	124K	165K	499K	475K	357K	562K	432K	422K	866K	261K	576K
			Varian	VA 183A	205K	294K	715K	698K	665K	634K	619K	590K	562K	549K	511K
1.4 to 2.5	6410-1 thru 6415-1	651-S1 651C-S1 651K-S1 651CK-S1	Stewart	SE 219-3	107K	340K	442K	576K	887K	412K	464K	1.15M	374K	412K	1.15M
			Varian	VA-186AA											
1.7 to 4.2	6420-1 thru 6423-1	652-S5 652C-S5 652K-S5 652CK-S5	Stewart	SE 215-6	178K	102K	280K	261K	205K	187K	210K	154K	147K	137K	162K
			Varian	VA-184AA											
2 to 4	6420 thru 6425	652 652C 652K 652CK	Stewart	SE 215A-2	124K	133K	383K	332K	340K	280K	301K	267K	232K	221K	232K
			Varian	VA 184A	215K	274K	953K	887K	536K	604K	619K	634K	562K	487K	634K
3.5 to 6.75	6430-1 thru 6435-1	653-S1 653C-S1 653K-S1 653CK-S1	Stewart	SE 221-1	100K	121K	402K	383K	243K	301K	237K	374K	130K	340K	174K
			Varian	VA 185AA	226K	340K	768K	909K	866K	649K	576K	649K	562K	562K	453K
3.7 to 8.3	6430-2 thru 6433-2	653-S2 653C-S2 653K-S2 653CK-S2	Stewart	SE 211B-7	158K	110K	316K	261K	226K	221K	178K	165K	154K	124K	110K
			Varian	VA 185AB	205K	147K	402K	374K	324K	294K	267K	237K	165K	412K	140K
4 to 8	6430 thru 6435	653 653C 653K 653CK	Stewart	SE 211A-8	133K	133K	392K	340K	294K	274K	243K	215K	200K	187K	154K
			Varian	VA 160A	215K	196K	499K	357K	324K	287K	261K	215K	178K	143K	143K
7 to 11	6440 thru 6443	654 654C 654K 654CK	Varian	VA 173A	187K	866K	976K	1.15M	1.02M	953K	649K	698K	634K	576K	499K
7 to 12.4	6440-1 thru 6443-1	654-S1 654C-S1 654K-S1 654CK-S1	Varian	VA 173A	191K	274K	619K	536K	475K	402K	365K	316K	249K	249K	169K
			Watkins-Johnson	WJ 2039-3	82.5K	154K	357K	357K	301K	301K	255K	243K	215K	200K	182K
8 to 12.4	6450 thru 6453	655 655C 655K 655CK	Varian	VA 173E	133K	465K	715K	649K	549K	536K	475K	392K	357K	294K	280K
			Watkins-Johnson	WJ 2039-2	634K	340K	453K	464K	442K	365K	324K	442K	267K	324K	243K
10 to 15.5	6460	656	Varian	VA 177A	137K	549K	750K	715K	487K	536K	619K	453K	499K	549K	249K
12.4 to 18	6470 6471	657 657K	Varian	VA 162A	133K	—	1.40M	768K	1.40M	604K	866K	536K	590K	715K	604K
18 to 26.5	6480	658	Varian	VA 163A	150K	1.40M	1.24M	953K	909K	750K	825K	634K	649K	442K	324K
26.5 to 40	6490	659	Varian	VA 164A	110K	464K	634K	619K	549K	475K	442K	392K	294K	357K	243K

* C suffix indicates PIN DIODE Attenuator **All Varian tubes use Anode protection circuits.

† K suffix indicates rf leveling sampler mounted internally. Basic model requires an external sampler for leveling.

SYM	DESCRIPTION	NO	DATE	BY
A	01 & 02 REPLACED 01A & 01B SN 30 & UP	12700	4-15-65	MN
B	SCHEMATIC ERROR R27 WAS SHOWN AS 1.5K	12778	7-16-65	mfc



NOTES:

1. UNLESS OTHERWISE SPECIFIED:
   1A. ALL RESISTORS IN OHMS, ½W, 1% METAL FILM
   1B. ALL CAPACITORS IN MICROFARADS
2. REFER TO INSTRUCTION MANUAL FOR SPECIFIC VALUE OF R1 THRU R11 FOR THE PARTICULAR MODEL INVOLVED
3. SQUARES (□) DESIGNATE PRINTED CIRCUIT CONNECTION ON ASSY NO. 950---
4. PREFIX REFERENCE DESIGNATORS BY "A13" (I.E: A13R1, A13C2)

ITEM	REQD	PART NO	DESCRIPTION

LIST OF MATERIAL

3/N 11-UP	650
NEXT ASSY	USED ON

MATERIAL

DRAWN D. Jew 11-4-64
ENG Ken A. Hansen 11-8-65
PROD D. Linden -65

MATERIAL STOCK NO

FINISH OR TREATMENT

SCALE

SCHEMATIC DIAGRAM DIODE SHAPER

DIMENSIONS ARE IN INCHES
TOL XX ± 015 XXX ± 005 ANGLE ± ½°
UNLESS INDICATED OTHERWISE

ALFRED ELECTRONICS
PALO ALTO CALIFORNIA

900713C

SHEET 1 OF 1

ALFRED

Tube Warranty and Replacement Procedure

Tube Replacement

All TWT and BWO tubes furnished in ALFRED instruments are available from ALFRED ELECTRONICS or the tube manufacturer for either warranty or out-of-warranty replacements. Current prices are given on the Replacement Tube Price List.

Tube Warranty

On ALFRED supplied BWO, TWT, and VTM tubes used in catalog instruments, ALFRED ELECTRONICS extends the full warranty given by the tube manufacturer and offers the following minimum:

Warranty for Microwave Associates West, ITT, & Litton TWT's, and Varian-Bomac VTM's

Tubes are warranted unconditionally against defects in workmanship, materials, and construction within the period shown in Column A and the number of heater hours shown in Column B. To supplement the unconditional warranty, the tubes are warranted on a pro-rata basis for the rated number of heater hours shown in Column C or for one year, both computed from the date of original shipment by ALFRED, whichever expires first. The pro-rated adjustment will be based on the current list price of the tube and the unused percentage of heater hours (Column C) or one-year time period, whichever results in lesser credit.

Warranty for MEC TWT's and All BWO Tubes

Tubes are warranted unconditionally against defects in workmanship, materials, and construction within the period shown in Column A and the number of heater hours shown in Column B, both computed from the date of original shipment by ALFRED. The adjustment will be based on the current list price of the tube.

Mfg.	Power	A months	B hours	C hours
		TWT		
MAW	1 - 10 mw	6	500	2000
MEC	1 - 20 mw	12	2000	
MAW	1 watt	6	250	1000
MEC	1 - 10 watt	12	1000	
ITT	10 watt	2	50	500
Litton	10 watt	-	50	500
		BWO		
All	0.5 - 40 GHz	12	2500	
		VTM		
Varian-Bomac	0.25 - 1 GHz	2	100	1000

WARRANTY CONDITIONS APPLY ONLY TO TUBES LISTED ABOVE WHICH ARE USED IN ALFRED CATALOG INSTRUMENTS.

This warranty applies only to tubes sold after June 1, 1968.

Return Authorization

TO OBTAIN WARRANTY CREDIT THE TUBE SERVICE REPORT MUST BE COMPLETED AND RETURNED WITH THE TUBE.

Return Procedure

In case a tube or instrument fails, please contact the factory or your local sales representative for advice or authorization to return either the tube or the instrument to the factory. NO TUBE OR INSTRUMENT WILL BE ACCEPTED FOR REPAIR OR CREDIT ADJUSTMENT WITHOUT WRITTEN AUTHORIZATION AND ACCOMPANYING SERVICE REPORT.

When the authorization is received, pack the tube in a rigid container (use original carton if available) at least 4" larger than the tube in each dimension. Wrap the tube with 1/4" of soft padding material and heavy kraft paper before placing in container. Pack at least 2" of resilient dunnage such as rubberized hair, foam, or similar shock absorbing material completely around the tube. Mark the container "FRAGILE" and ship PREPAID via air freight, air express or rail express. Do not ship via parcel post or air parcel post since experience has shown that fragile items are more apt to be damaged when shipped via these methods.

Use similar wrapping, packing and shipping provisions for an instrument. At least 6" of rubberized hair (or equivalent shock absorbing material) is required for an instrument.

In-Warranty Failure - BWO and VTM Tubes

BWO and VTM tubes which fail in warranty will be replaced with a new tube. In case a tube fails, please issue a purchase order for a new tube at the tube list price and return the defective tube. Warranty credit will be issued and deducted from the price, subject to tube manufacturer's verification that failure was caused by tube malfunction. To qualify for maximum credit, it is essential that the defective tube be properly packed and promptly returned.

Repairable or In-Warranty Failure - TW Tubes

If the returned tube is found to be repairable, the user will be asked to issue a purchase order for repair charges determined by the original tube manufacturer. In general, the repair will be at no cost if the tube is in warranty and there is no evidence of misuse or abnormal conditions of operation.

Warranty Credit

Any applicable credit allowed under the terms of this warranty applies only toward the future purchase of a replacement tube from ALFRED ELECTRONICS. Cash settlements will not be made. In terms of this warranty, a replacement tube must be the same type as that on which credit is allowed. Credit for one tube only may be applied toward the purchase of a single replacement tube. Warranty credit remains in effect for a maximum of six (6) months from date ALFRED ELECTRONICS notifies the customer. All credits will be based on the purchase price of the tube which prevails at the time the order for a replacement tube is received by ALFRED ELECTRONICS.

BWO RF Cables

RF OUTPUT CABLE MUST ACCOMPANY BWO TUBES returned for warranty credit, repair or replacement. If rf output cable is NOT returned, the cable price is added to the normal BWO repair or replacement charge. For prices see Replacement Tube Price List.

Special Conditions

If a returned tube or instrument proves not to be defective after test and evaluation at ALFRED ELECTRONICS, the tube or instrument will be returned at the customer's expense.

The final determination as to whether the tube failed in normal operation thereby bringing it under the applicable warranty or whether the tube was misused thereby excluding it from application of the warranty provisions, lies solely with the tube manufacturer.

Data, warranty credit, and prices subject to change without notice.

6/7/68

ALFRED ELECTRONICS

Service Report

Instrument Model _____ Serial No. _____ Date Received _____

TW/BWO Tube Type _____ Serial No. _____ Date Received _____

Date First Tested _____ Date Placed in Service _____ Date of Failure _____

Total Number of Hours of TWT/BWO Heater Operation _____

_____

1. Was operation normal just prior to failure? Yes _____ No _____
2. If not, what was done to adjust it? _____

_____

_____
3. Has proper cooling been maintained during tube operation? _____

_____
4. Did helix overcurrent tripout occur? _____

_____
5. Was environment normal? (specify) _____

_____
6. Symptoms - (describe failure) _____

_____

_____
7. Operating conditions at time of failure (indicate any unusual conditions) _____

_____

_____
8. Name of person to contact regarding evaluation of failure _____

Telephone No. _____ Signature _____

Title _____

Date _____

SHIPMENT DAMAGE CLAIM

The instrument should be tested as soon as it is received. If it fails to operate properly or is damaged in any way, a claim must be filed with the carrier. Have carrier agent inspect damage to instrument and packing crate and forward us a copy of his written report. You will then be advised of replacement or repair arrangements and disposition of the equipment. Please include model number, type number and serial number when referring to this instrument for any reason.

WARRANTY

ALFRED products are warranted against defects in workmanship and materials. This warranty is limited to repair or replacement of products or parts (except tubes, transistors, and batteries) which prove defective within one year after delivery to original purchaser. ALFRED ELECTRONICS is not liable for consequential damage.

If any fault develops:

1. Notify us, giving full details of the difficulty, including the model number, type number and serial number. Upon receipt of this information, you will be sent service instructions or shipping data.

2. Upon receipt of shipping instructions, forward the instrument prepaid and repairs will be made at the factory. If requested, an estimate of the charges will be made before the work begins when instrument is not covered by this warranty.

PACKING

The instrument should be packed in the original carton. If unusable or not available, use a wooden box with approximately three inches of excelsior or other shock-absorbing material surrounding the instrument. If desired, packing services of a local transfer and storage company may be employed.

4.4
10

CHALLENGER CLASP NO. 28 K-55
6 x 9